

GAIA

Glob**A**l un**I**fied p**A**ge cache for Heterogeneous systems

Tanya Brokhman Pavel Lifshits Mark Silberstein

Technion, Israel Institute of Technology Tighter integration of GPU memory into the OS page cache and file I/O mechanisms is required!

mmap() for GPUs Disciplined CPU-GPU file sharing





Improved performance





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Consistency model considerations



Strong consistency unsuitable!

Up to 27X performance degradation due to false sharing









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A distributed, weakly-consistent page cache architecture for heterogeneous multi-GPU systems

Implements Lazy Release Consistency

Extends OS Page cache to GPU memory

- New CPU and GPU I/O optimizations
 - ✓ Up to 3X speedup in CPU file I/O
 - ✓Up to 8X speedup in unmodified realistic workloads

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Track 1, on Thursday July 11





