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zpoline: a system call hook mechanism based on binary rewriting

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User-space program



User-space program



User-space program

system call

System Call Hook

- System calls are the primary interface for user-space programs to communicate with OS kernels
- A system call hook mechanism intercepts a system call

User-space program	
system call	
Kernel-space OS subsystem	

System Call Hook

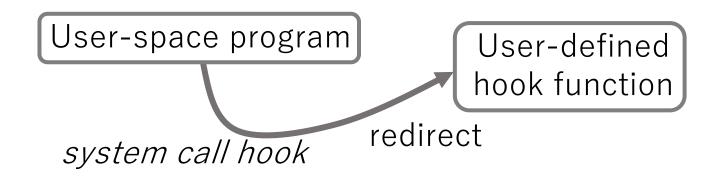
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User-space program

intercept system call hook

System Call Hook

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- A system call hook mechanism intercepts a system call, and redirects the execution to a user-defined hook function



 System call hook mechanisms allow us to <u>transparently</u> apply <u>user-space OS subsystems</u> to existing applications

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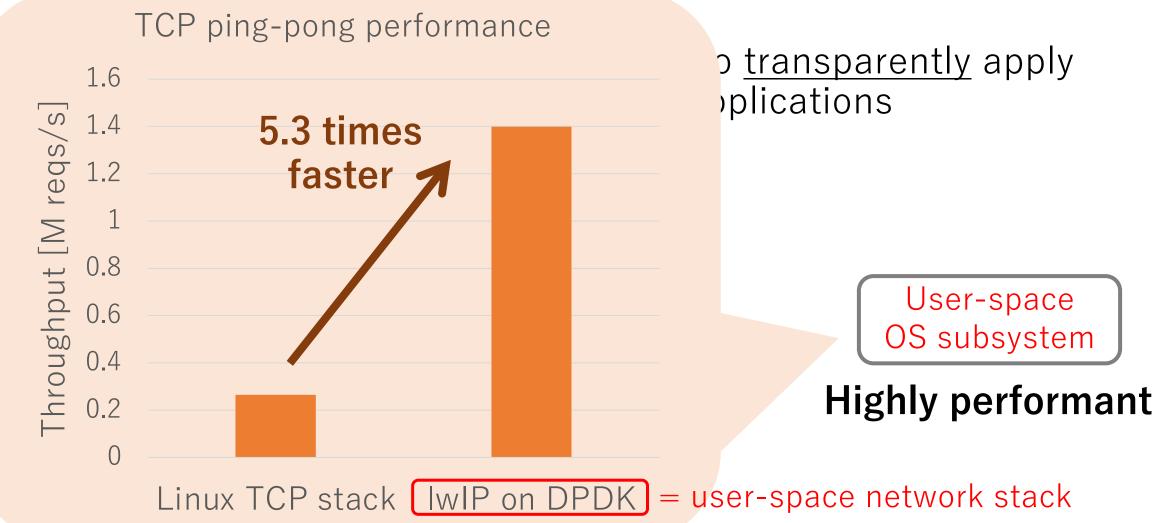
User-space program

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Kernel-space OS subsystem

User-space OS subsystem

Highly performant



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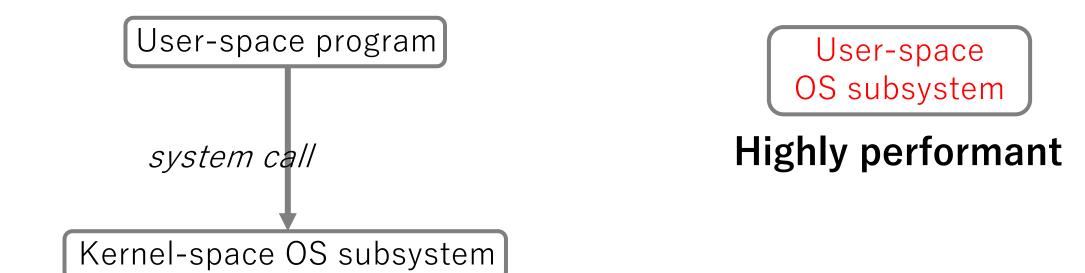
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Kernel-space OS subsystem

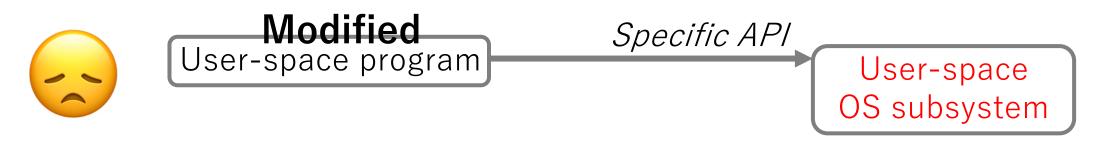
User-space OS subsystem

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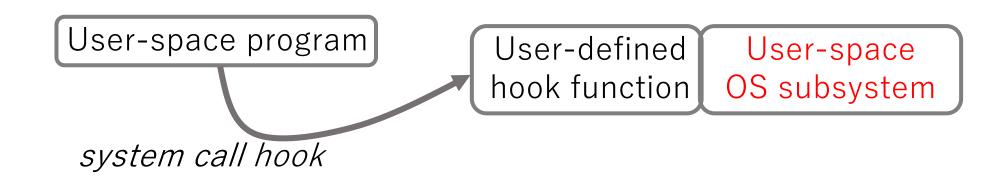
We need to change the program

Highly performant

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 - If we use a system call hook mechanism, ...

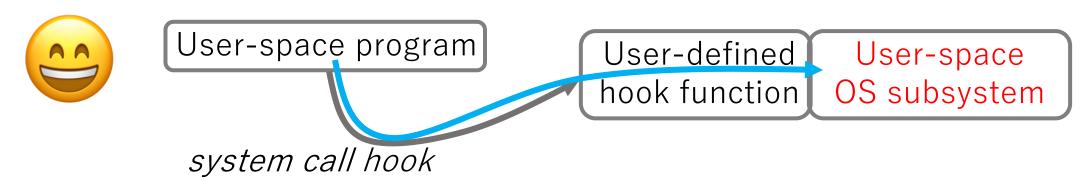
User-space program	User-space
	OS subsystem
system call	
Kernel-space OS subsystem	16

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Existing Mechanisms

- ptrace
- int3 signaling technique
- Syscall User Dispatch (SUD)
- LD_PRELOAD trick
- Binary rewriting techniques

ms allow us to <u>transparently</u> apply to existing applications **ok mechanism, ...** user-space program is necessary

> User-defined hook function

User-space OS subsystem

Kernel-space OS subsystem

am



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System call hook

Kernel-space OS subsyst

IwIP on DPDK : TCP ping-pong

Compared to

LD PRELOAD

14.7% 17.0%

SUD PRELOAD

1.6

Throughput [M reqs/sec] 1.4 1.5 0.0 0.4 0.5 0

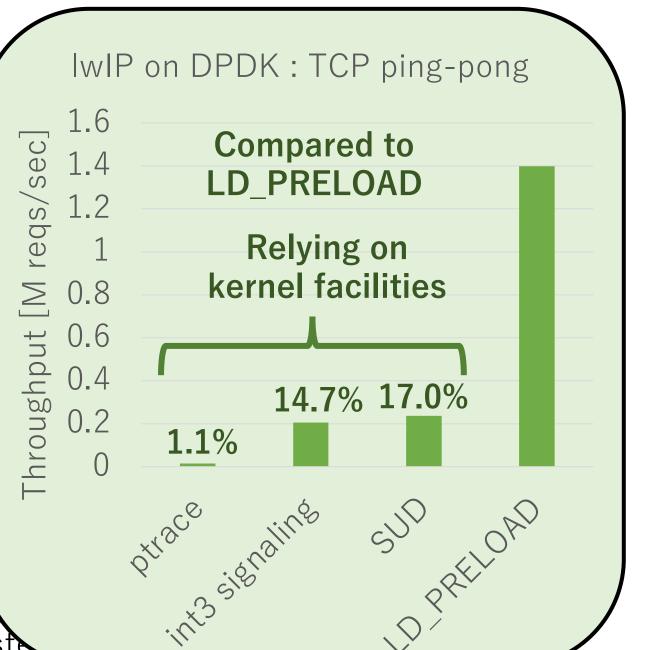
1.1%

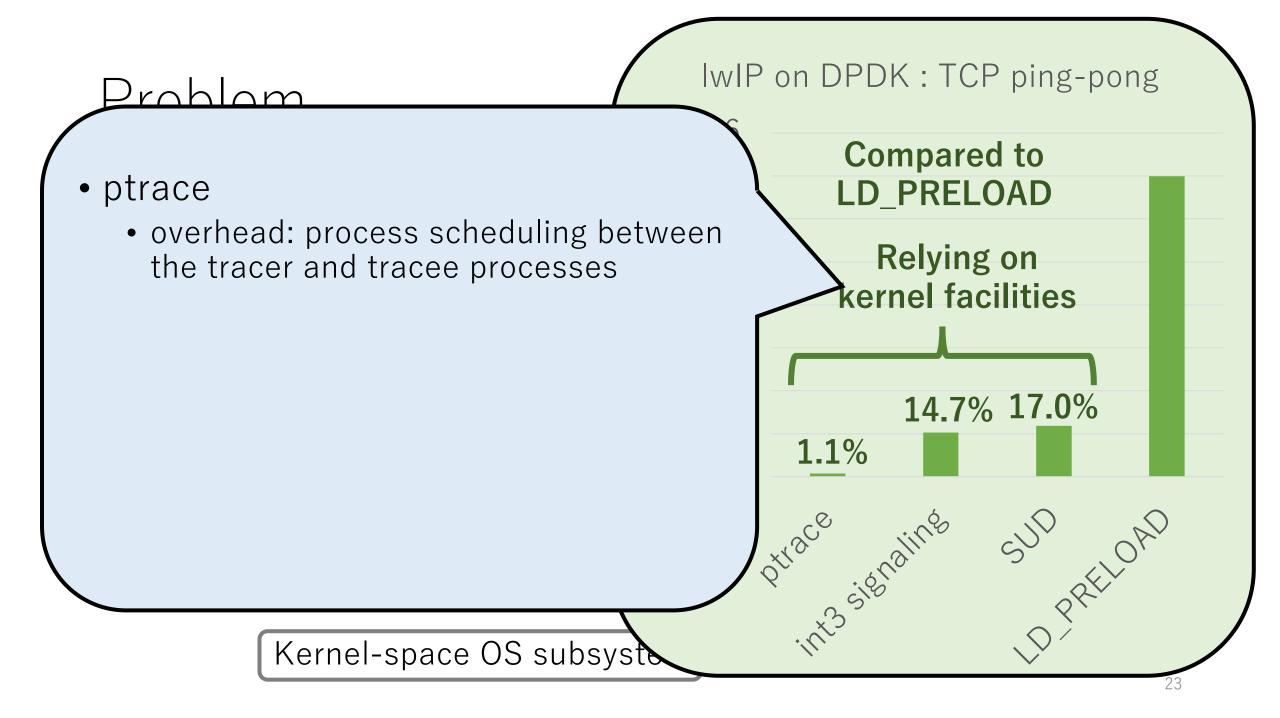
ptrace signalines

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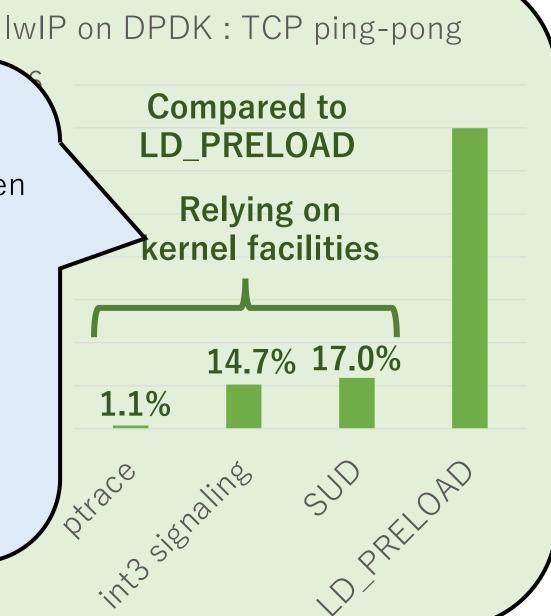


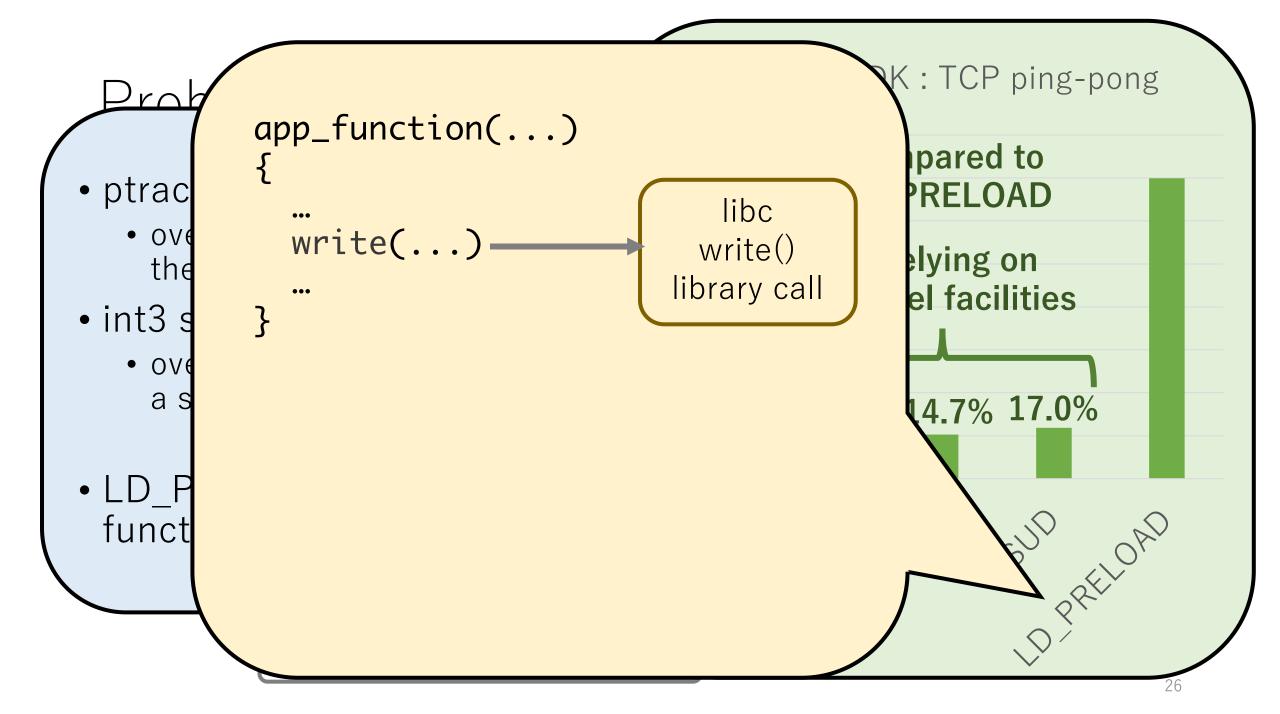
IwIP on DPDK : TCP ping-pong rohlom **Compared to** • ptrace LD PRELOAD • overhead: process scheduling between **Relying on** the tracer and tracee processes kernel facilities int3 signaling / SUD • overhead: context manipulation for a signal() handler (SIGINT/SIGSYS) 14.7% 17.0% 1.1% ptrace signalines SUP PRELOA Kernel-space OS subsyste

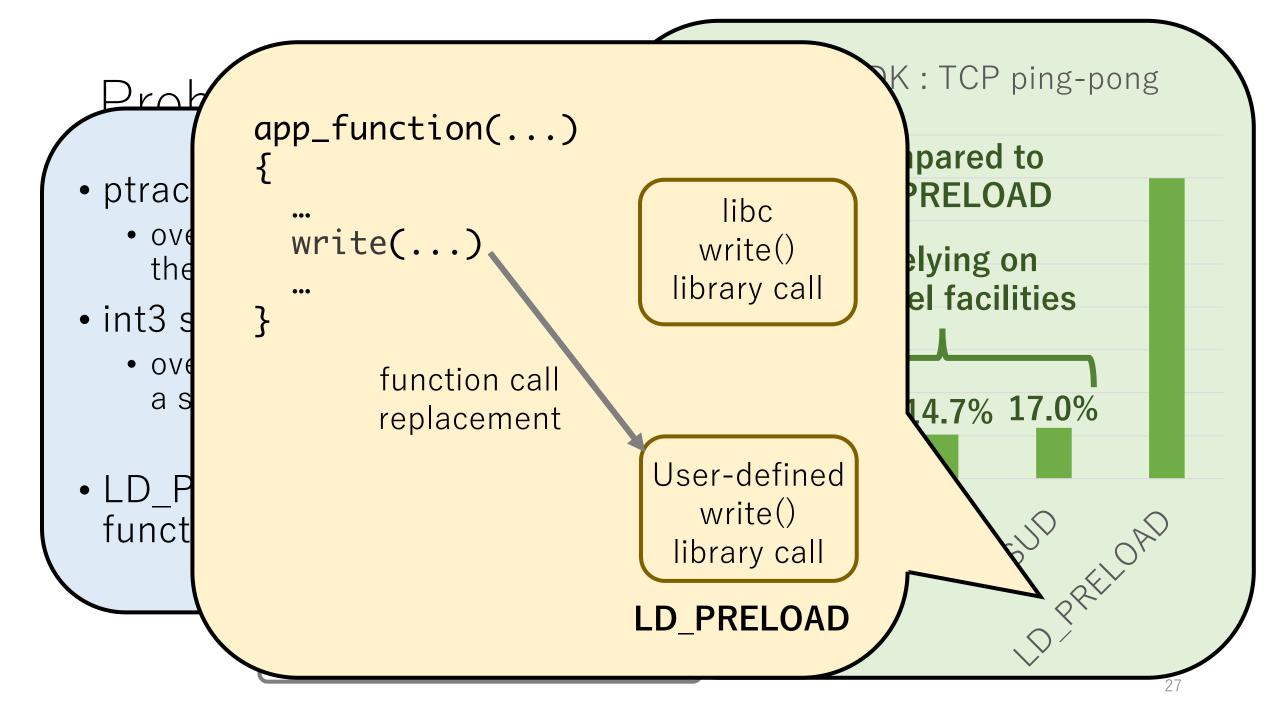
Problam

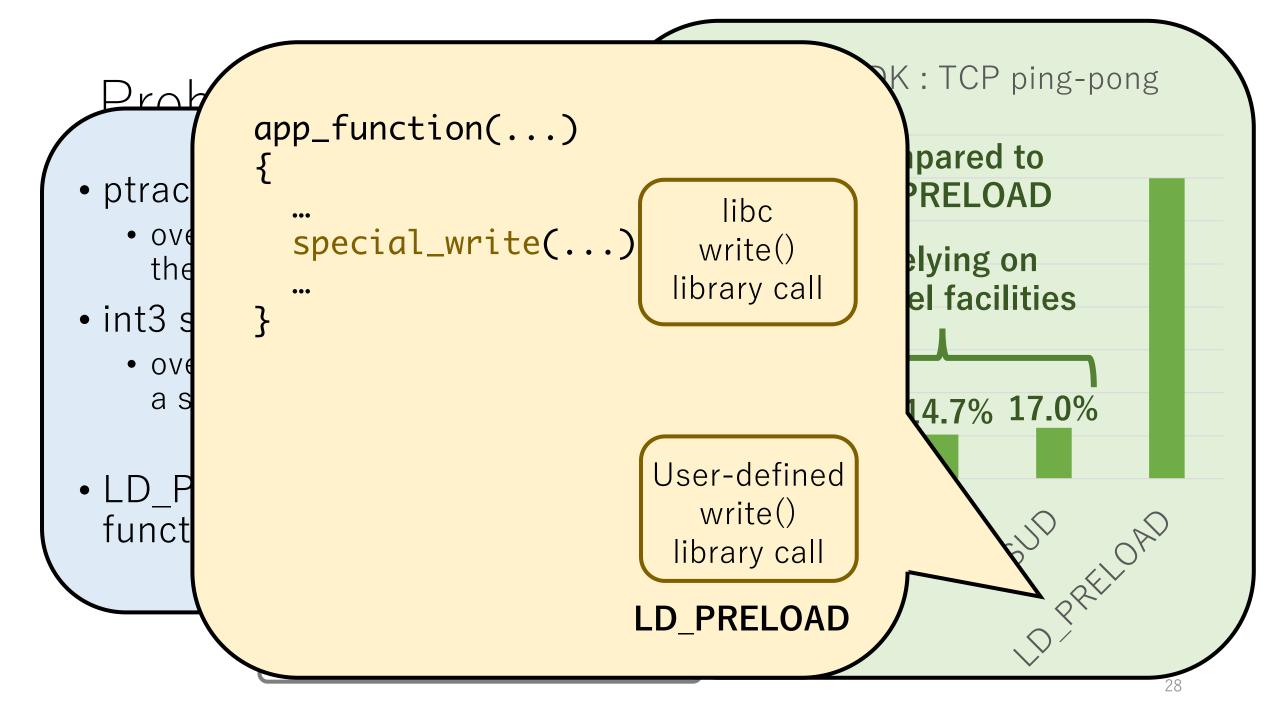
• ptrace

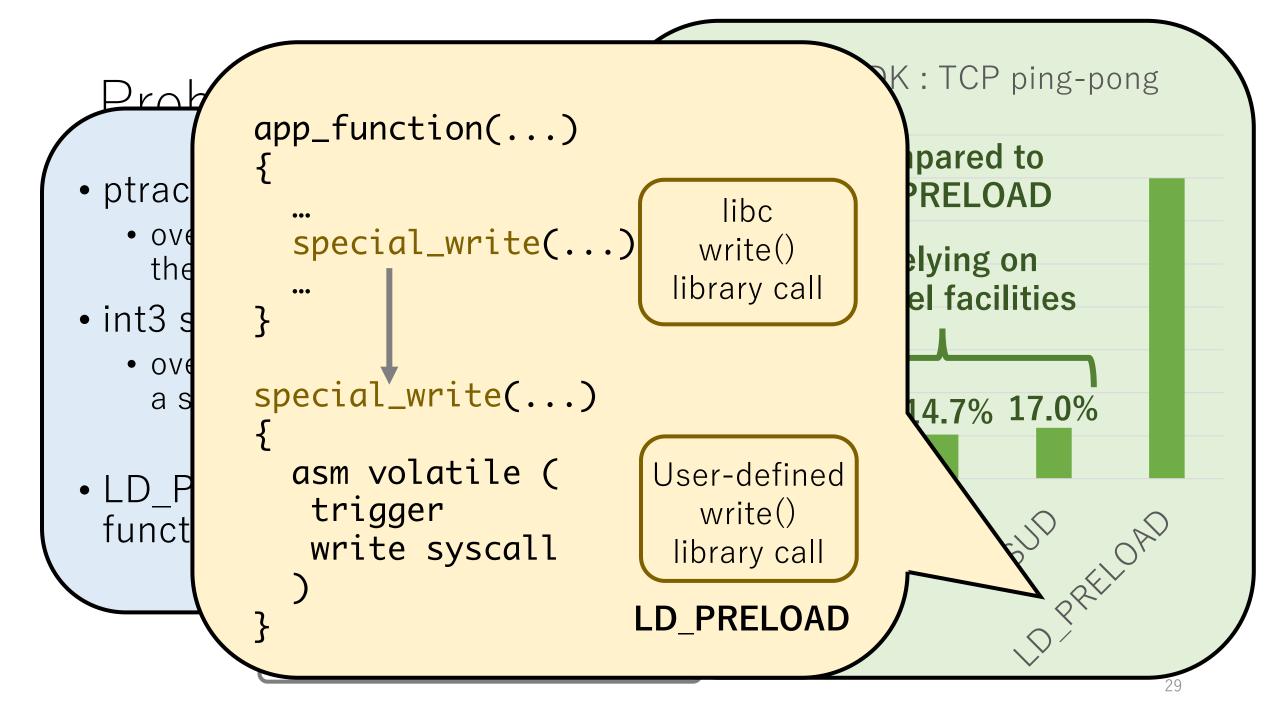
- overhead: process scheduling between the tracer and tracee processes
- int3 signaling / SUD
 - overhead: context manipulation for a signal() handler (SIGINT/SIGSYS)
- LD_PRELOAD just replaces function calls, therefore, it is fast

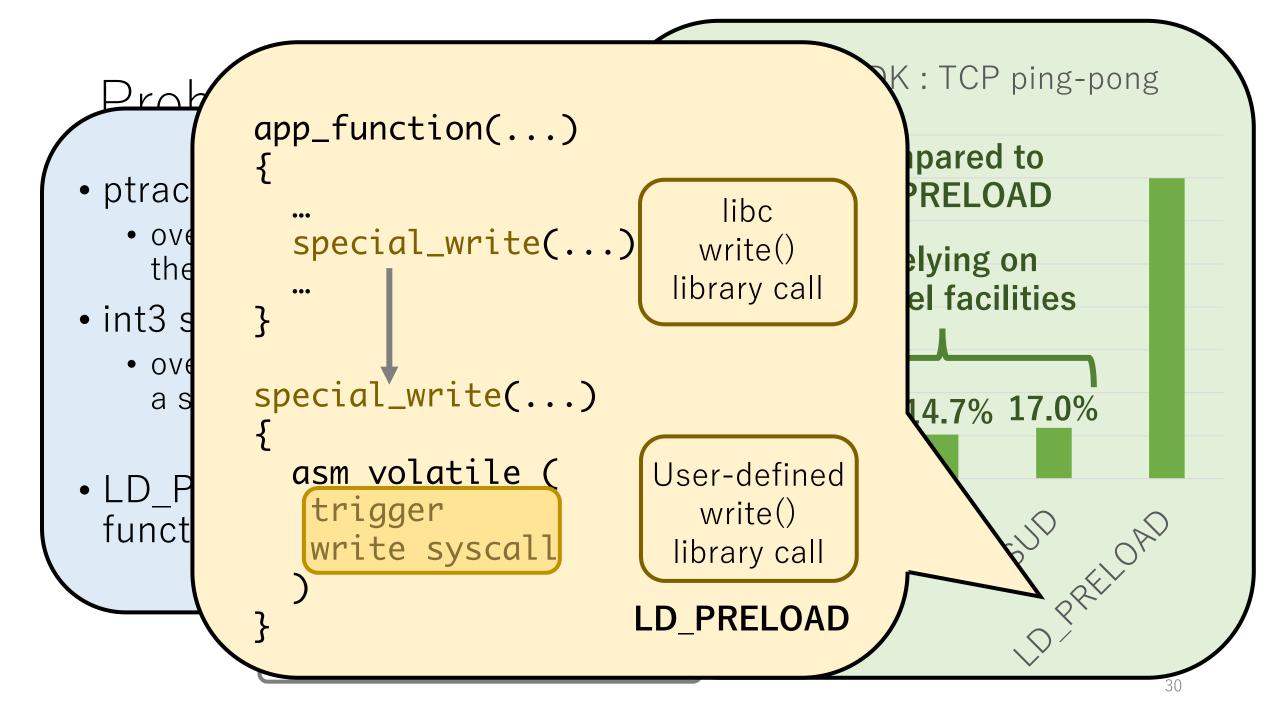


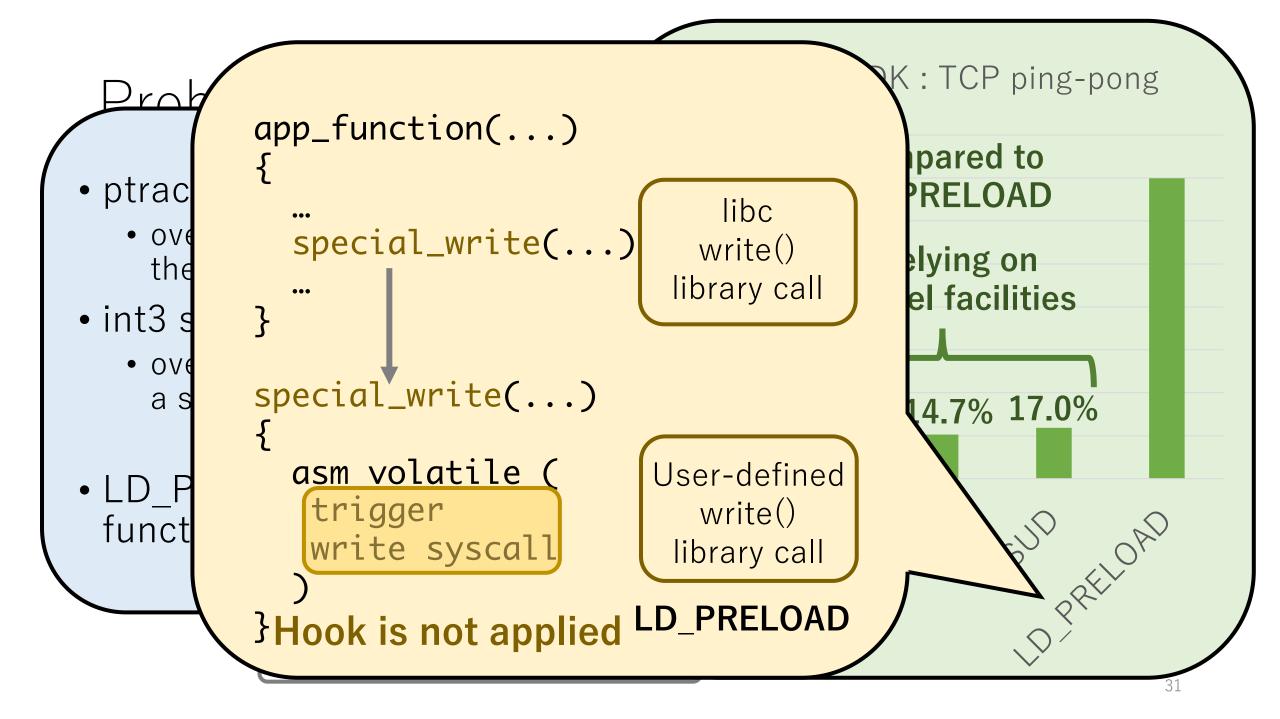


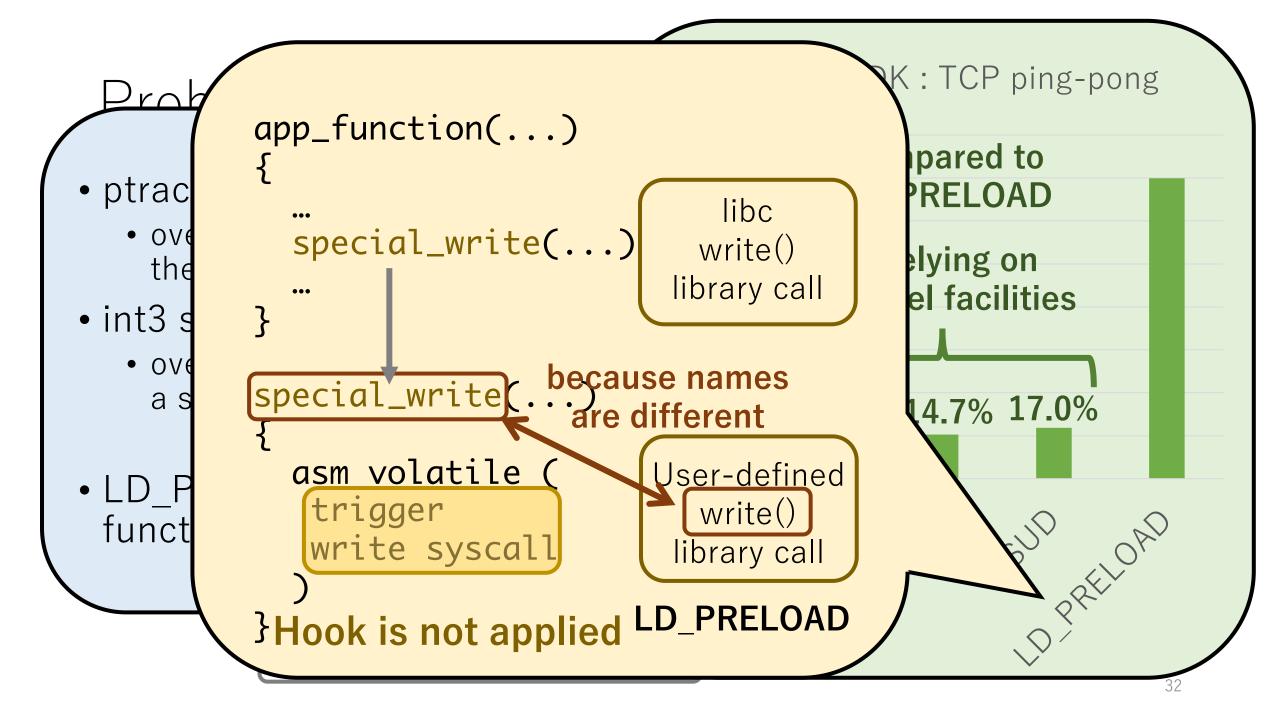


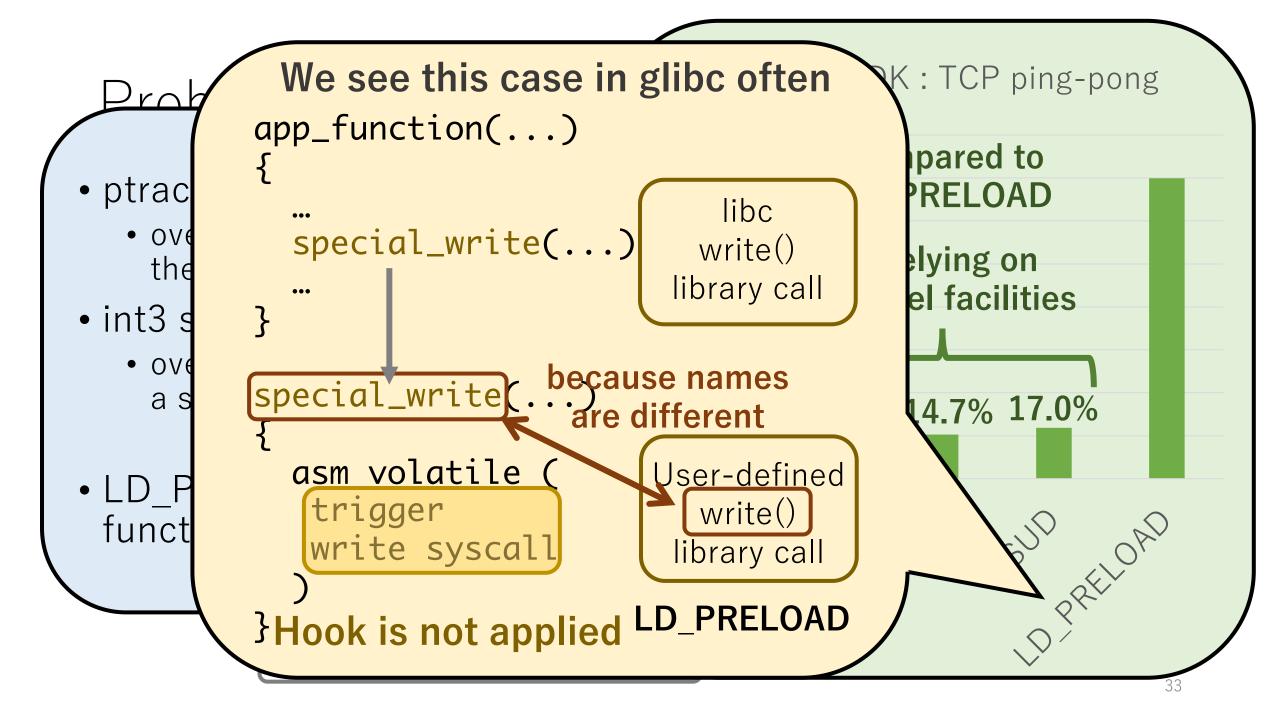












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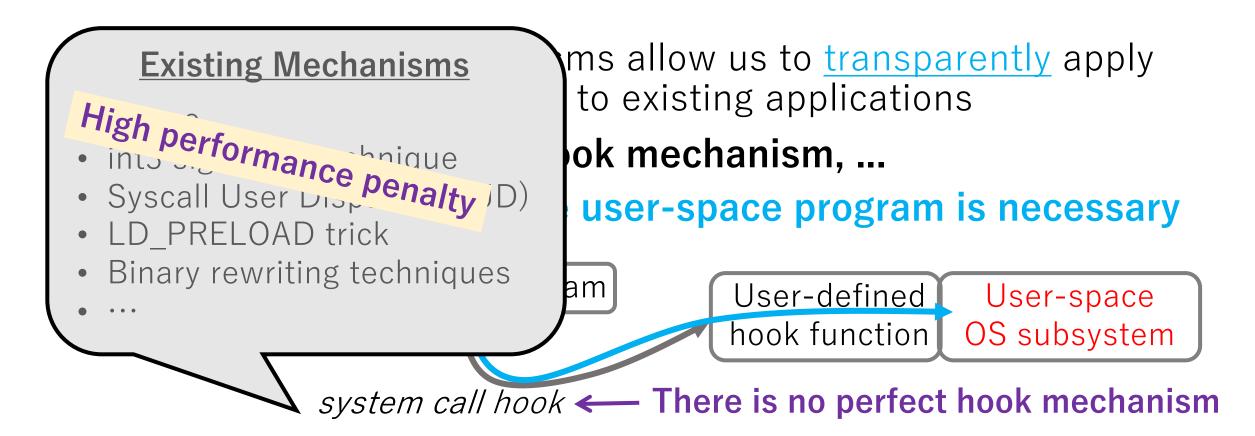
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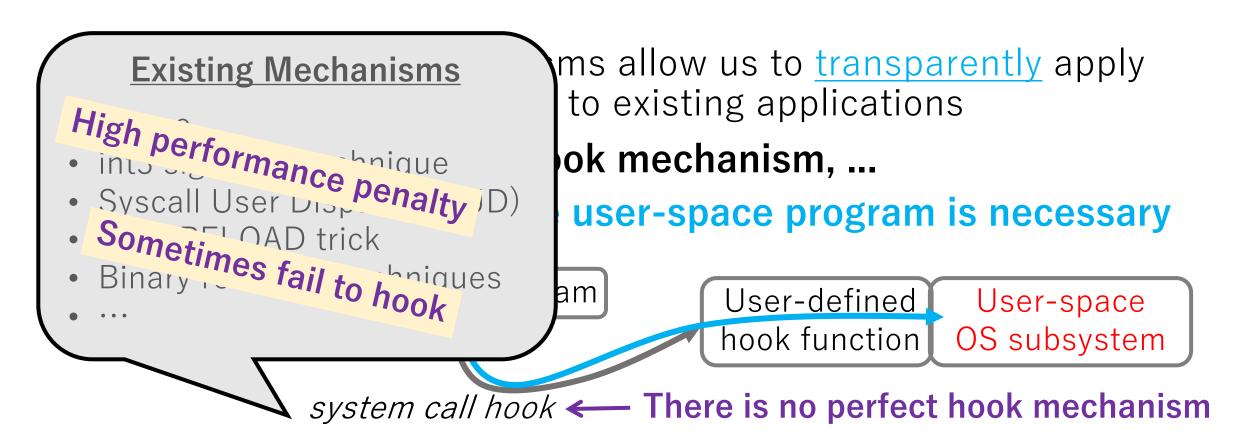
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User-space OS subsystem

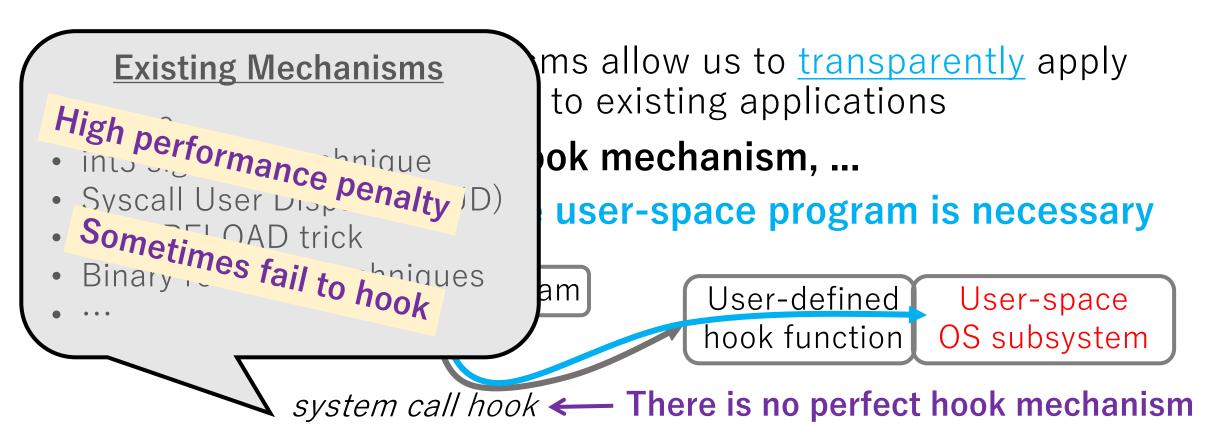
Kernel-space OS subsystem

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Problem Applicability of user-space OS subsystems has been limited regardless of their benefits

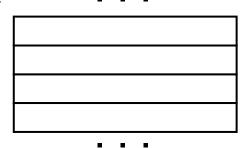


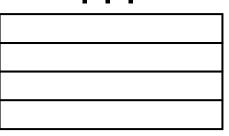
Kernel-space OS subsystem

Contribution

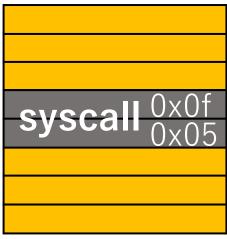
- zpoline: a system call hook mechanism for x86-64 CPUs
 - based on binary rewriting
 - free from the drawbacks of the pervious mechanisms
- This work addresses a challenge that is specific to binary rewriting approaches

- Virtual Memory 0x0000 0x0001 0x0002
- On x86-64 CPUs, syscall and sysenter instructions trigger a system call
 - syscall: 0x0f 0x05, sysenter: 0x0f 0x34

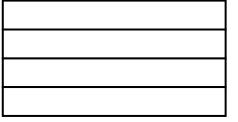




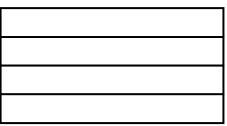
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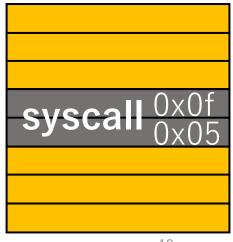
Virtual Memory 0x0000 0x0001 0x0002



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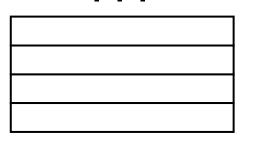


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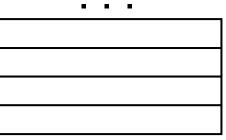
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- What we wish to achieve

trigger a system call

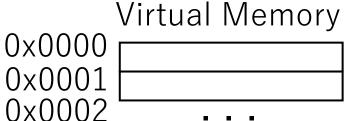






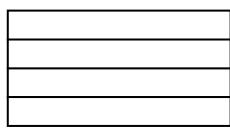
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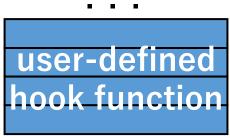
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 - replace syscall/sysenter instruction with something

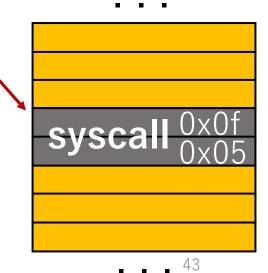


Virtual Memory

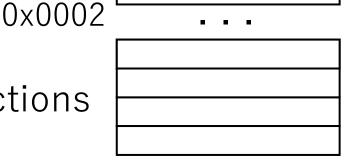
0x0000

0x0001



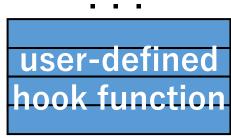


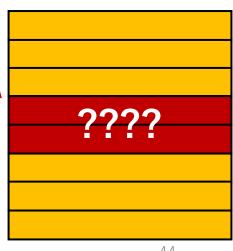
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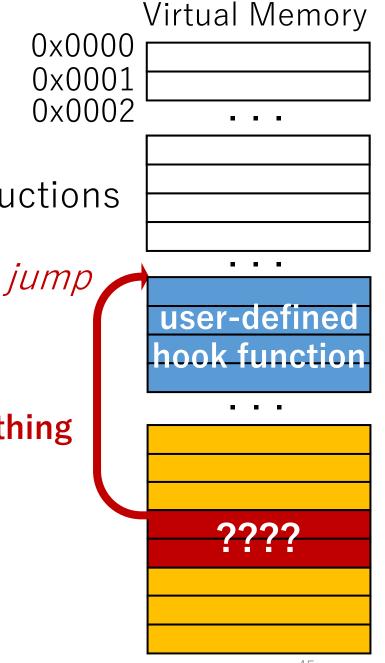
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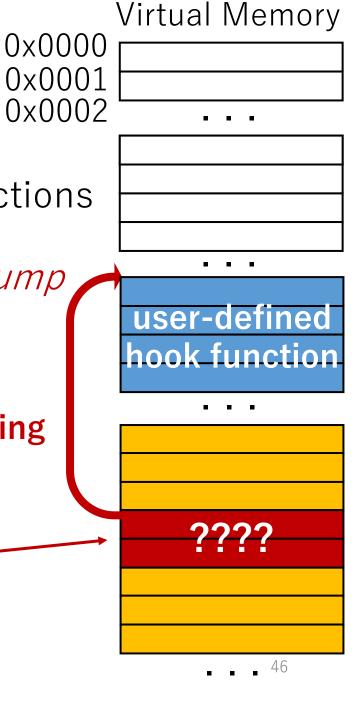




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 - to jump to a user-defined hook function



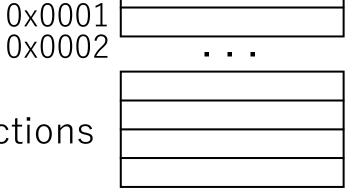
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 - to jump to a user-defined hook function
- Question: what should we put here?



jump

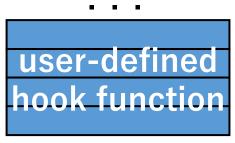
Challenge

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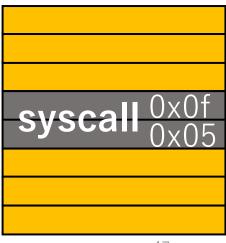


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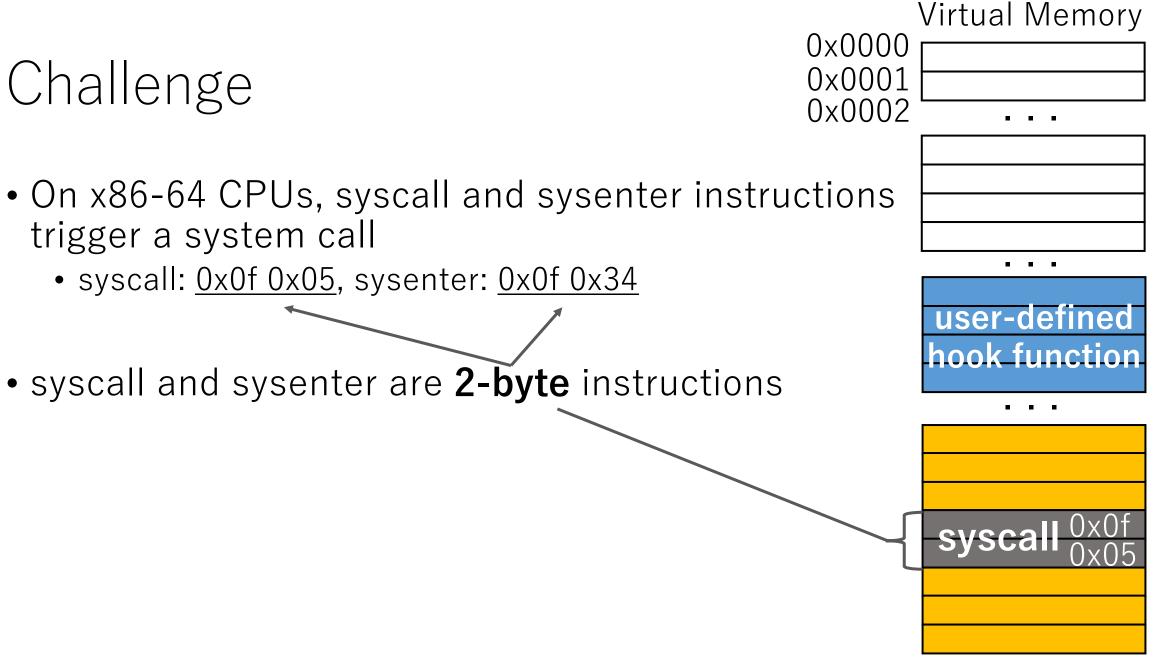
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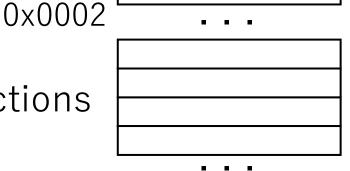






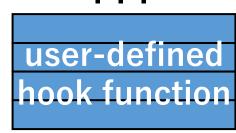
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 - syscall: <u>0x0f 0x05</u>, sysenter: <u>0x0f 0x34</u>
- syscall and sysenter are **2-byte** instructions
- Specification for a jump destination address needs more than 2 bytes

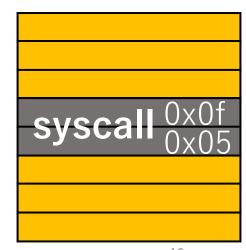


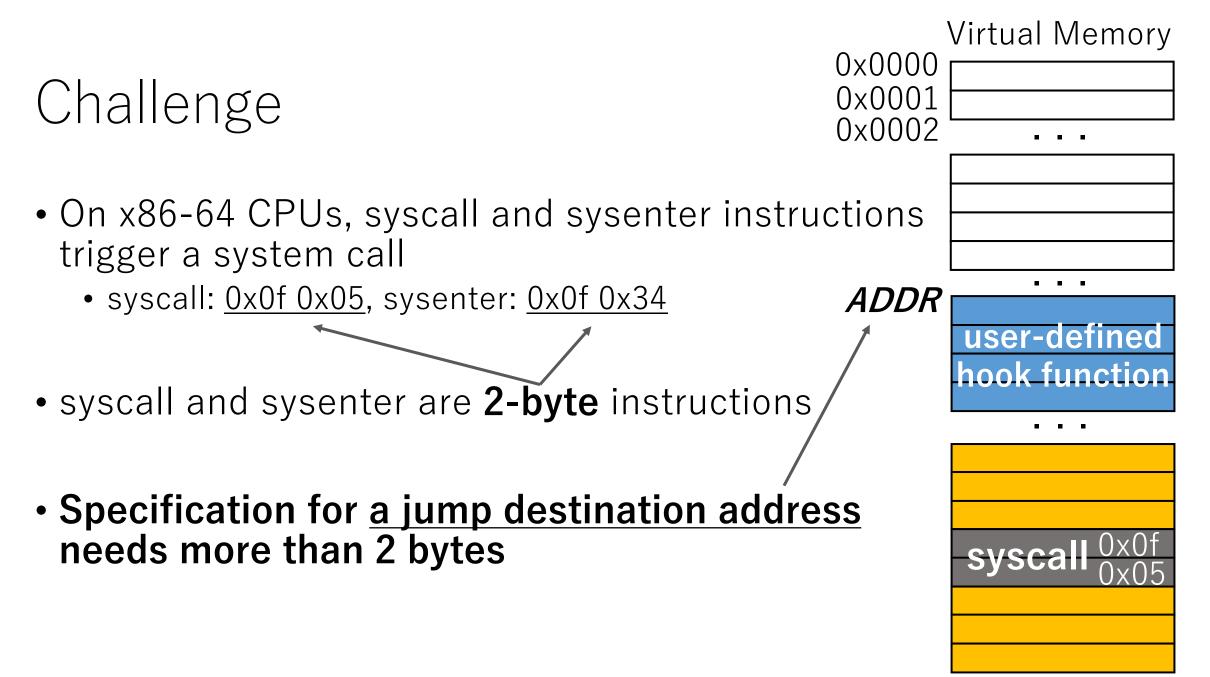


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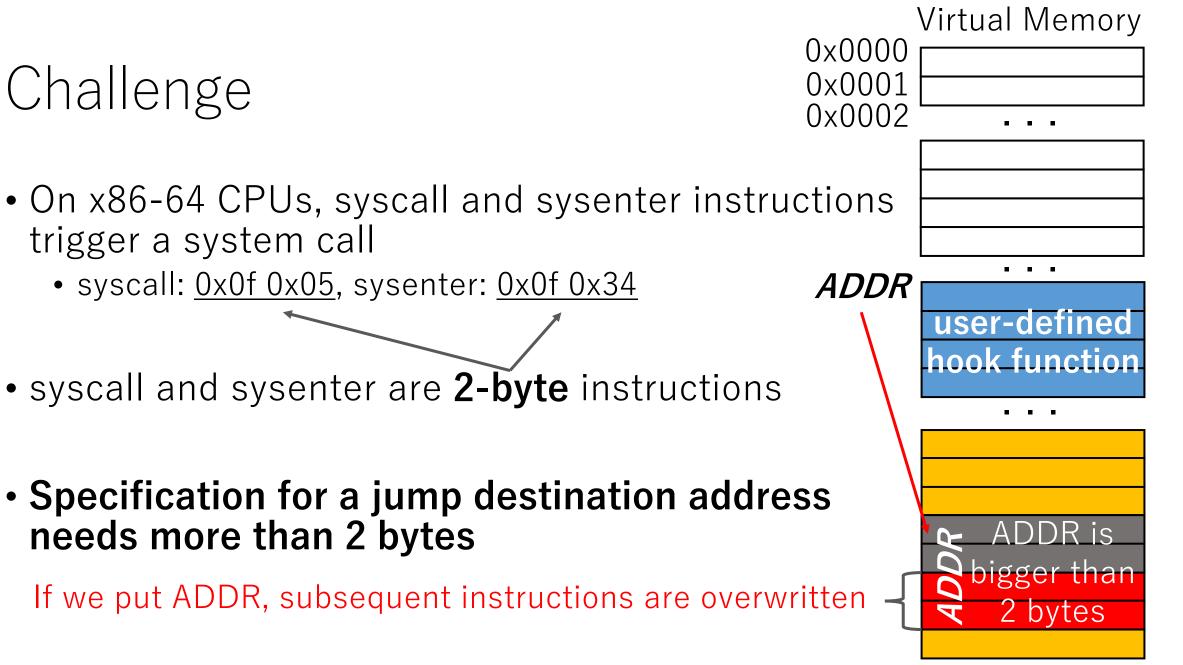
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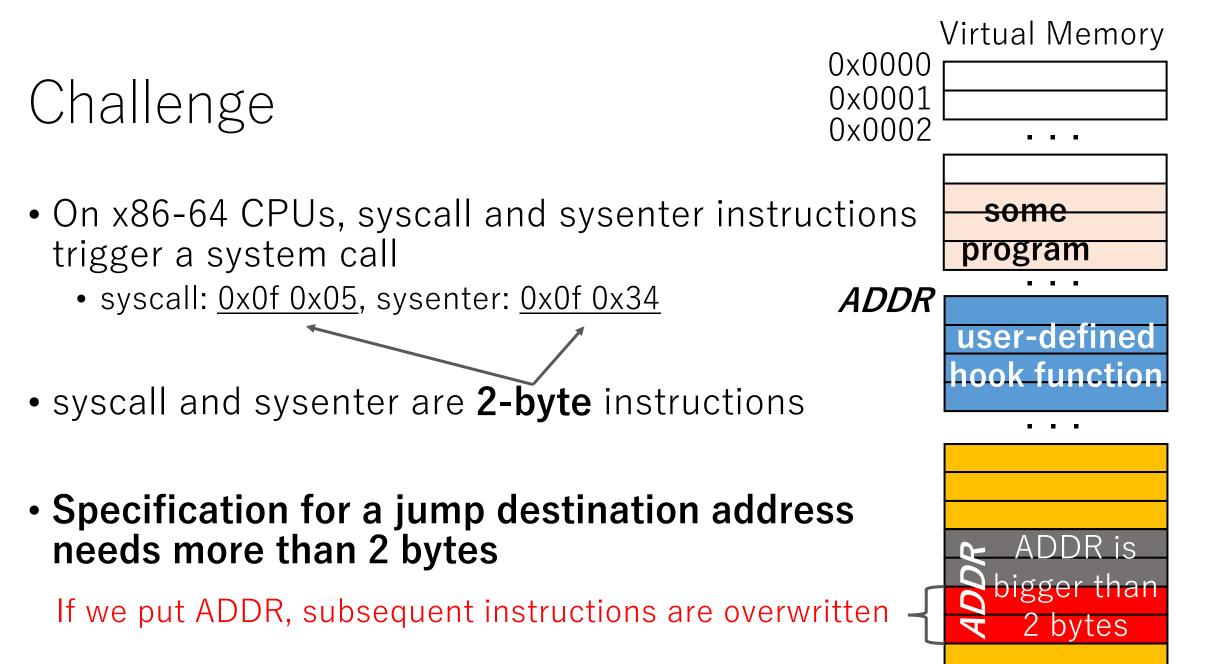


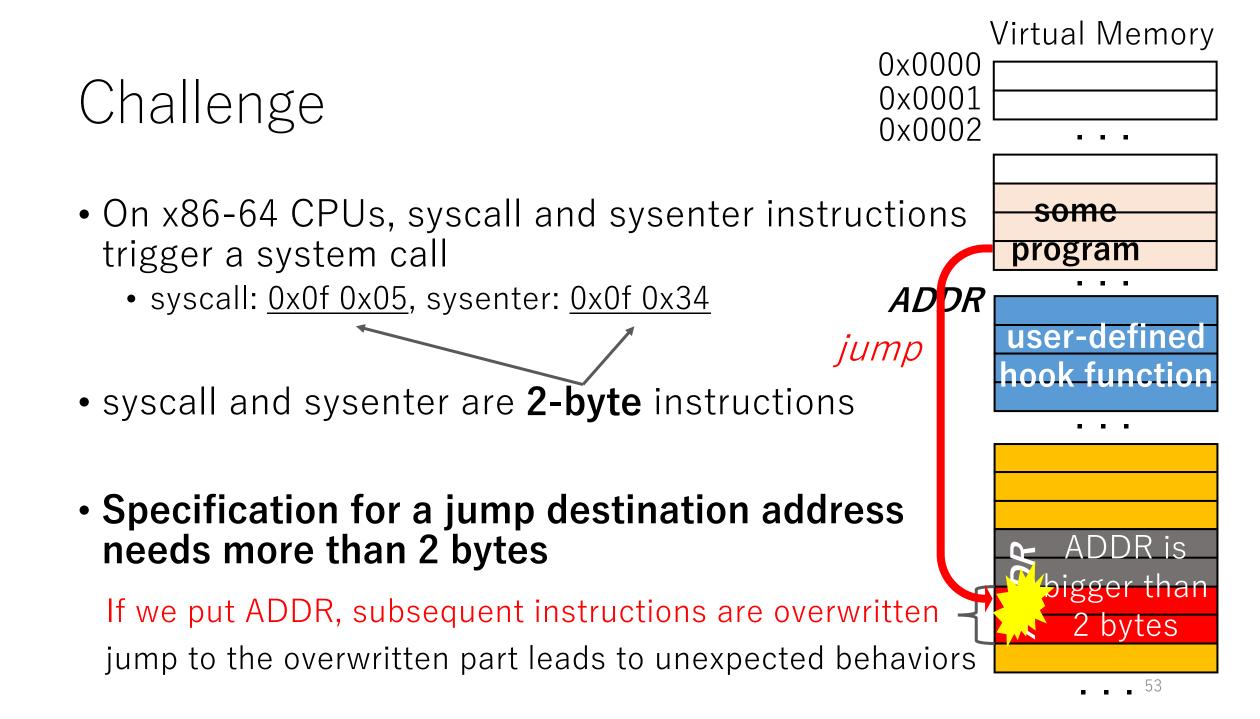


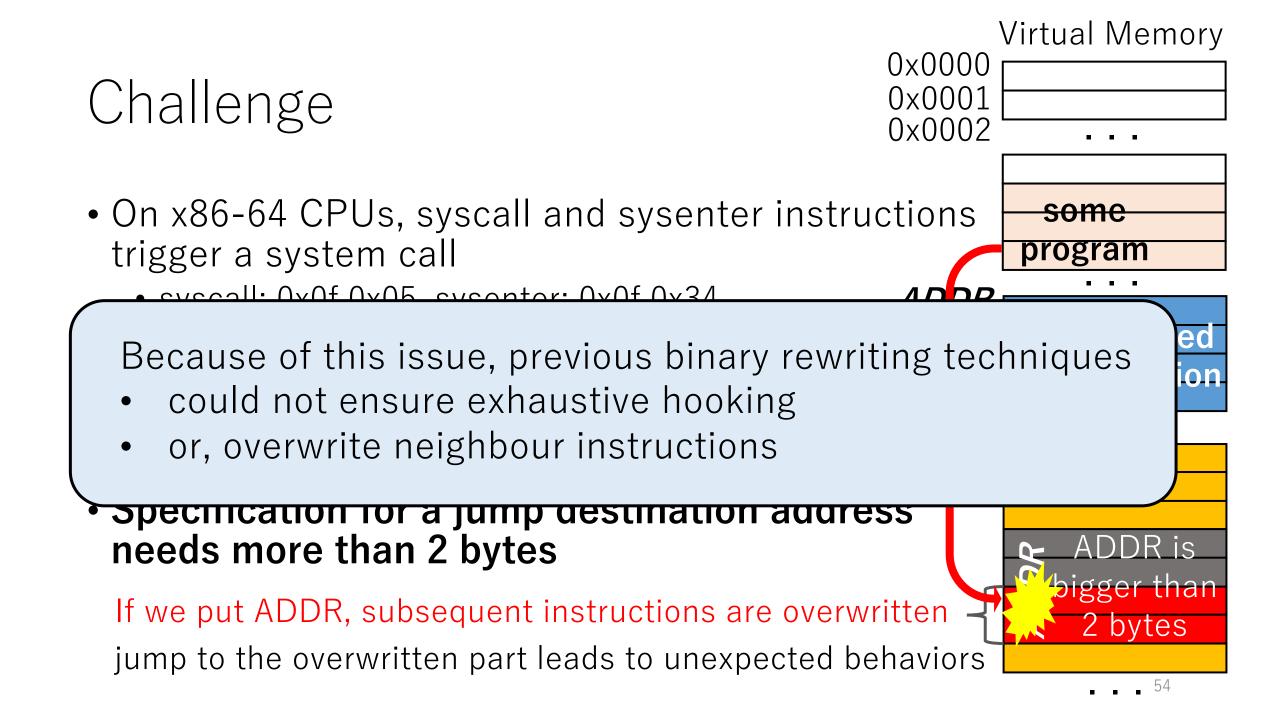


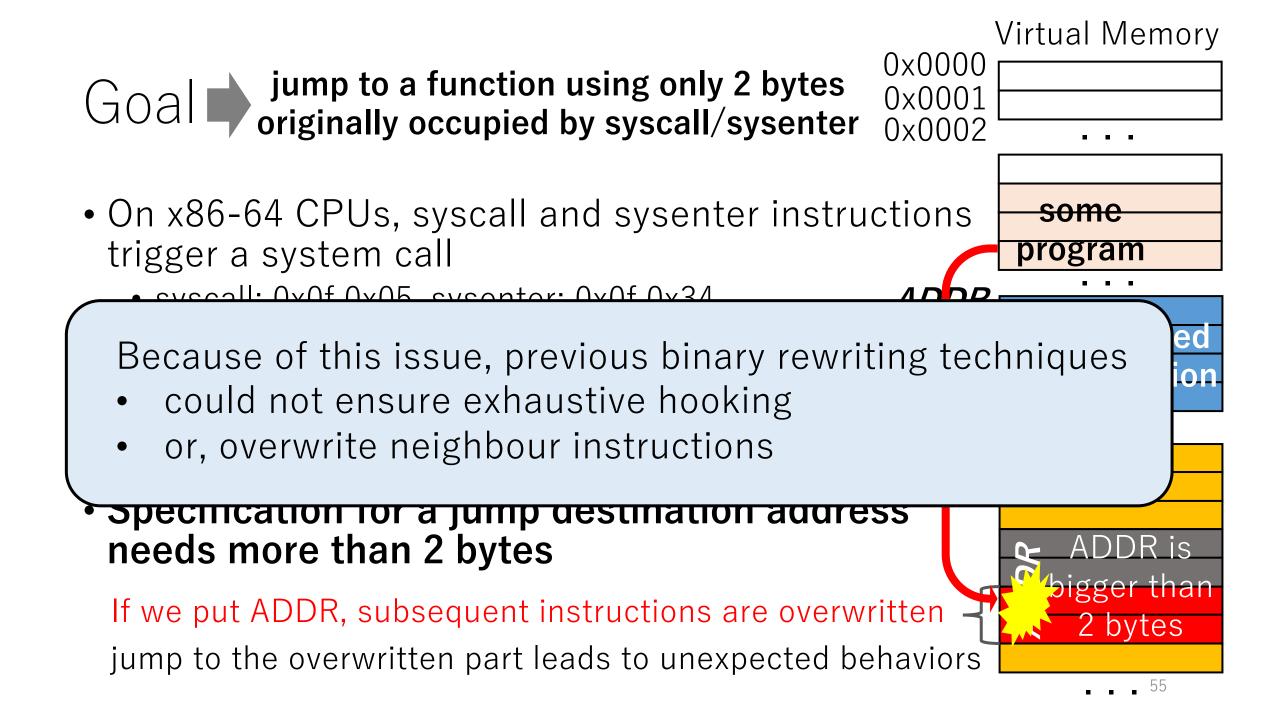
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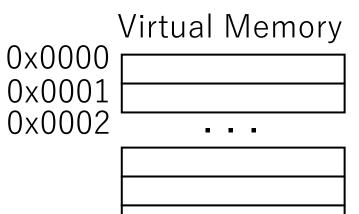


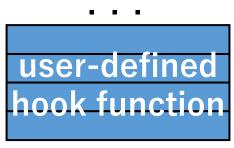




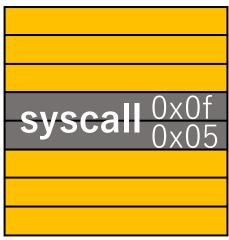


• How to invoke a system call

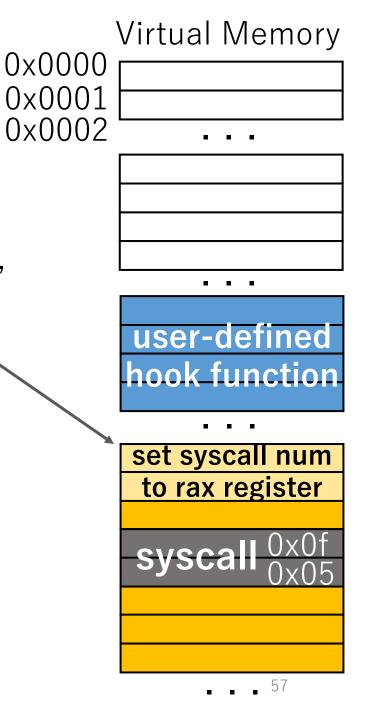




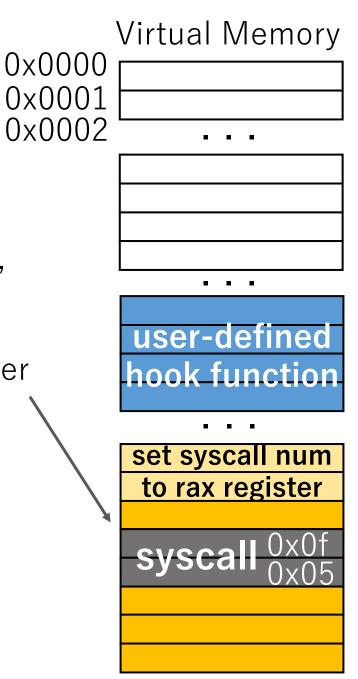
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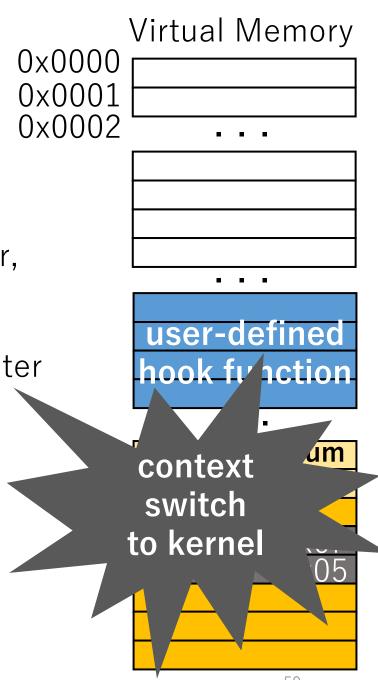
- How to invoke a system call
 - A user-space program sets a system call number, predefined by the kenel, to the **rax register**
 - e.g., 0: read(), 1: write(), 2: open(), ...



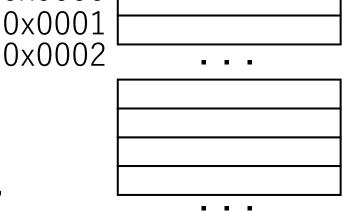
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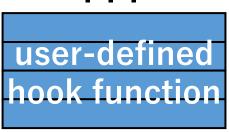


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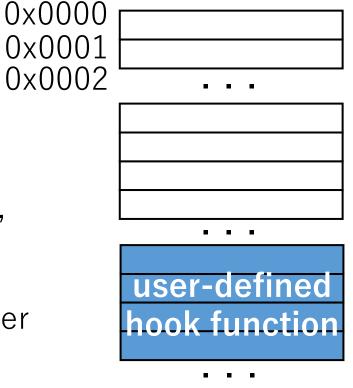
Virtual Memory

0x0000

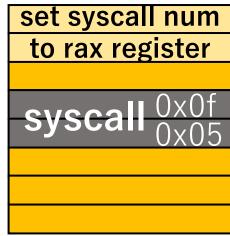




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 - The user-space program executes syscall/sysenter ---- the context is switched to the kernel ----
 - Kernel executes a system call specified through the system call number set to the **rax register**
 - if the rax register has 0, the kernel executes read()
 - if the rax register has 1, the kernel executes write()
 - if the rax register has 2, the kernel executes open()



Virtual Memory

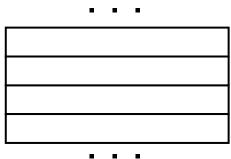


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Point: Calling Convention

When syscall/sysenter is executed, the rax register always has a system call number,

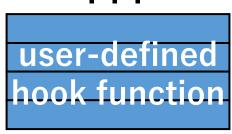


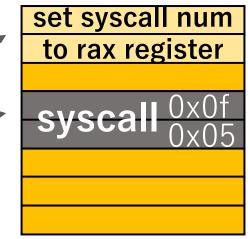
Virtual Memory

0x0000

0x0001

0x0002



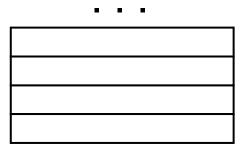


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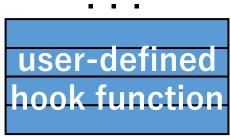
When syscall/sysenter is executed, the rax register always has a system call number, which is 0 ~ around 500 (defined in the kernel)

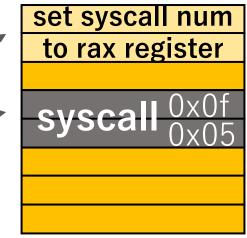


Virtual Memory

0x0000

0x0001



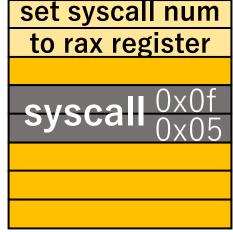


zpoline replaces syscall/sysenter with callq *%rax

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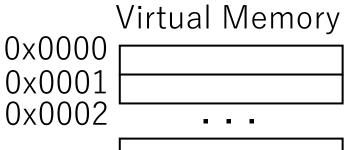
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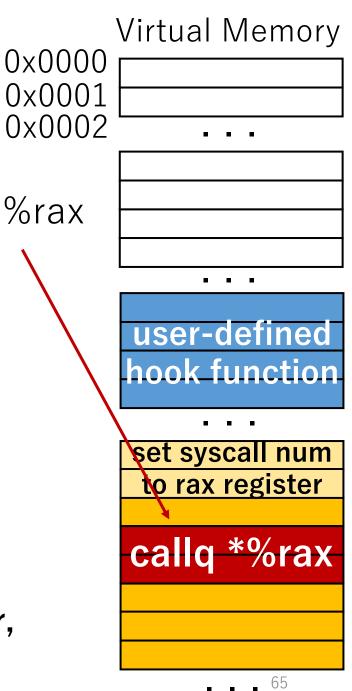
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user-defined



- zpoline replaces syscall/sysenter with callq *%rax
 - callq *%rax is a 2-byte instruction (0xff 0xd0)

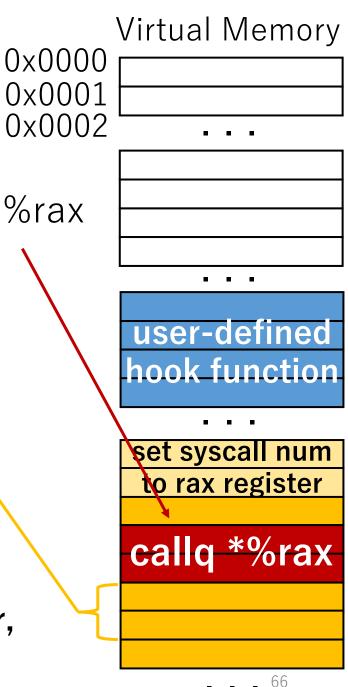
Point: Calling Convention





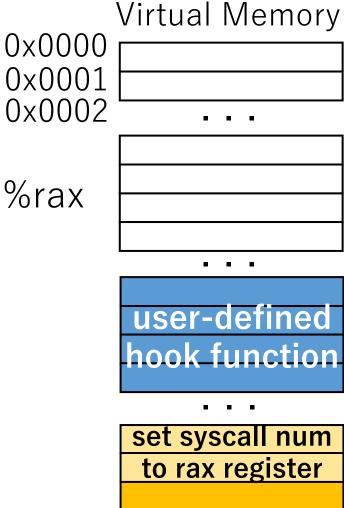
- zpoline replaces syscall/sysenter with callq *%rax
 - callq *%rax is a 2-byte instruction (0xff 0xd0)
 - Neighbour instructions are not overwritten

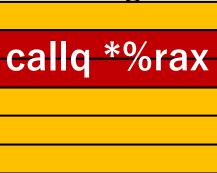
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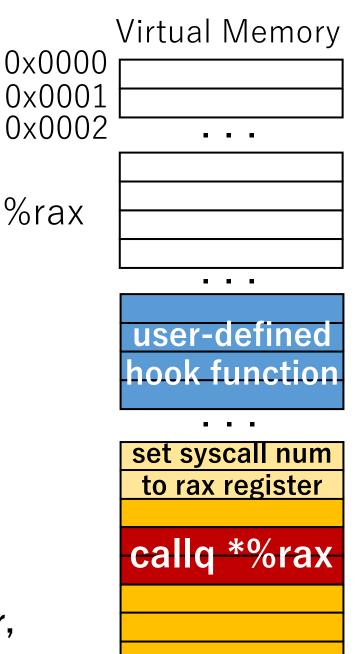
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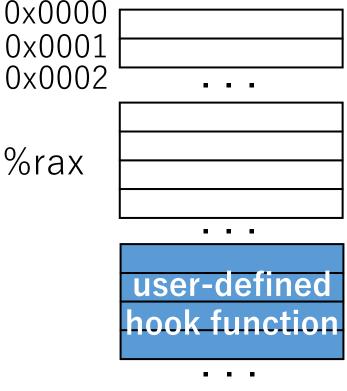


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After the binary rewriting

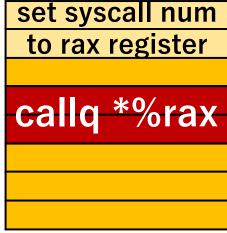
Point: Calling Convention

When syscall/sysenter is executed, the rax register always has a system call number, which is $0 \sim \text{around } 500$ (defined in the kernel)



Virtual Memory

0x0001

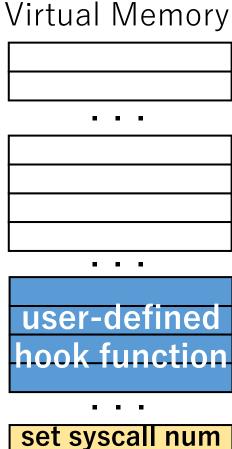


69

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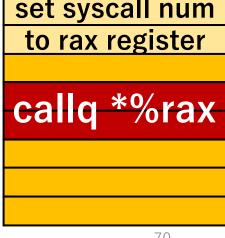
After the binary rewriting Point: Calling Convention

When syscall/sysenter callq *%rax is executed, the rax register always has a system call number, which is 0 ~ around 500 (defined in the kernel)



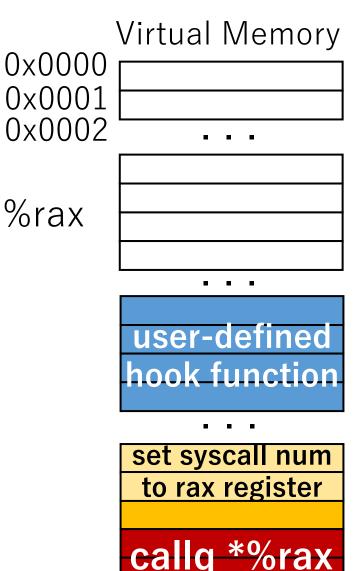
0x0000

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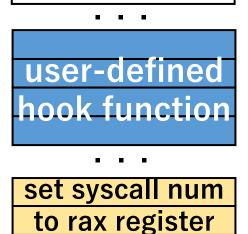
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callg *%rax

_ 72

Virtual Memory

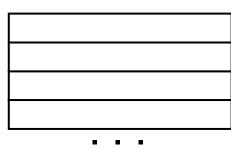
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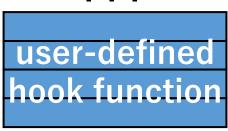


Virtual Memory

0x0000

0x0001

0x0002

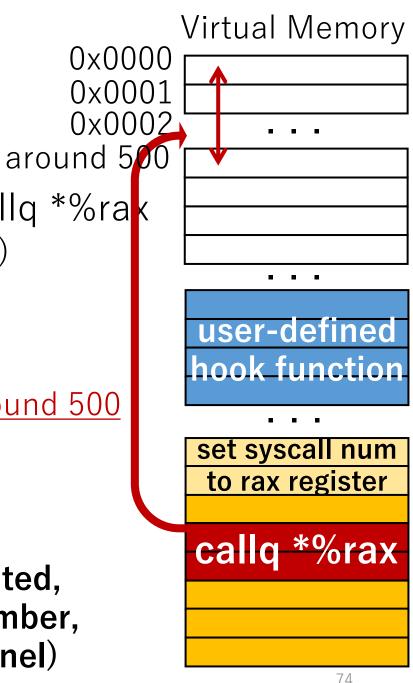


set syscall num to rax register Callq *%rax

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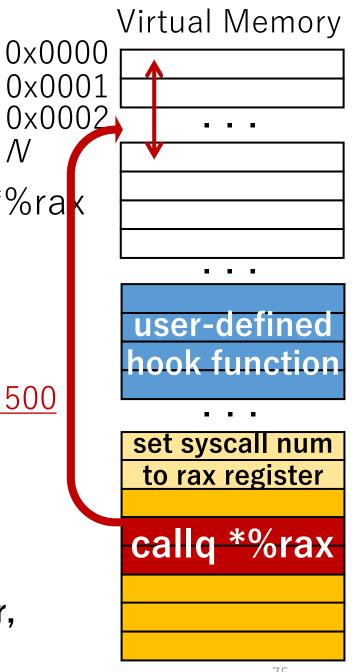


address range, potentially replaced "callq *%rax" jumps to (*N* is the max syscall number)

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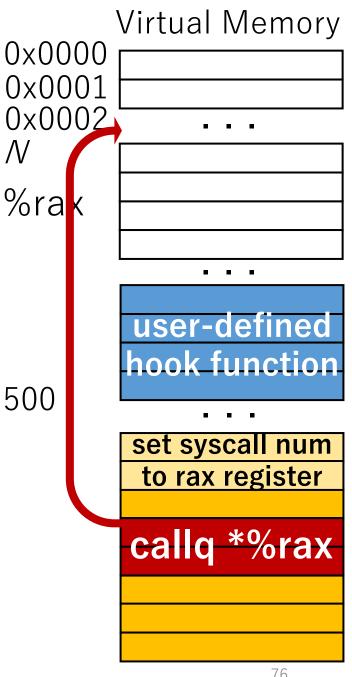
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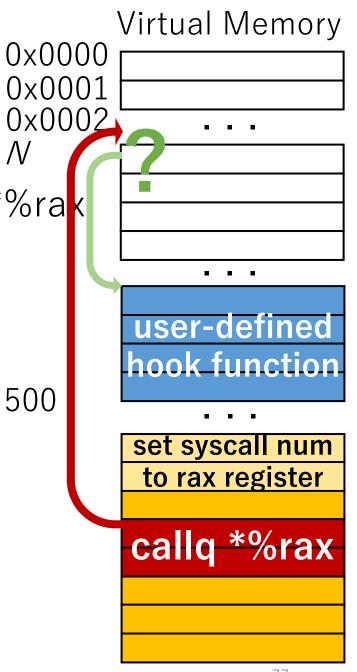
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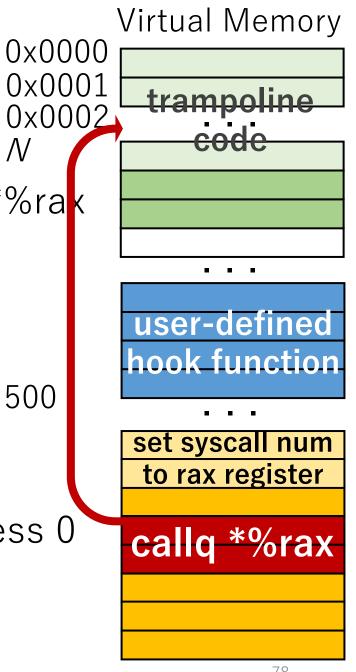


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How to redirect to the user-defined hook function?

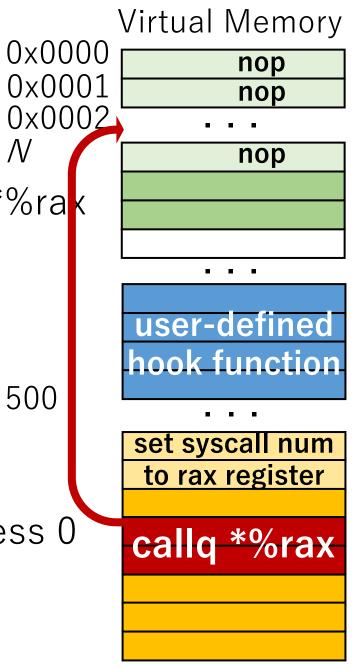
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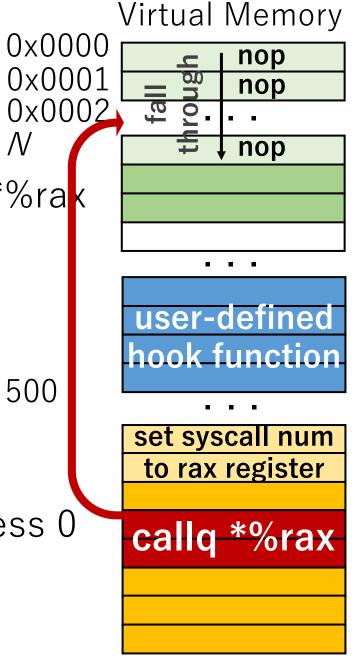
- zpoline instantiates trampoline code at address 0
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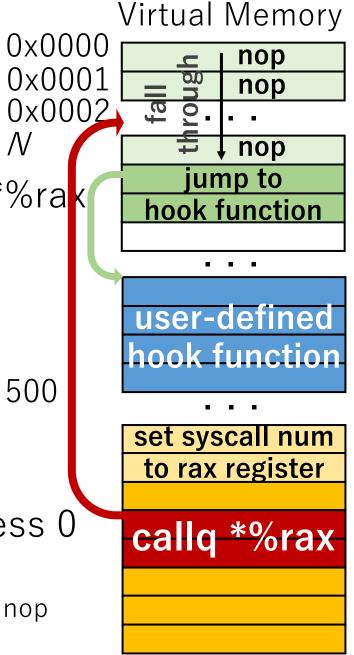
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- zpoline instantiates trampoline code at address 0
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 - puts code to jump to the hook function next to the last nop



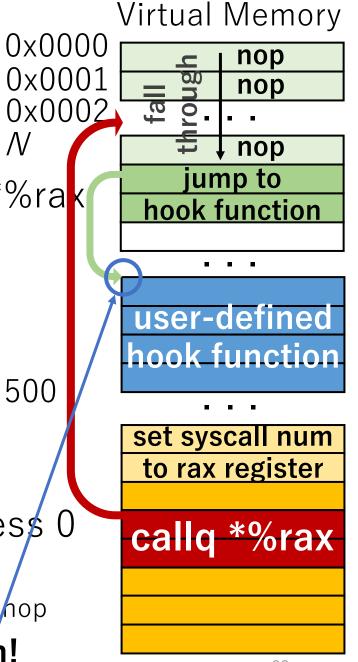
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We could reach the user-defined hook function!



Virtual Memory



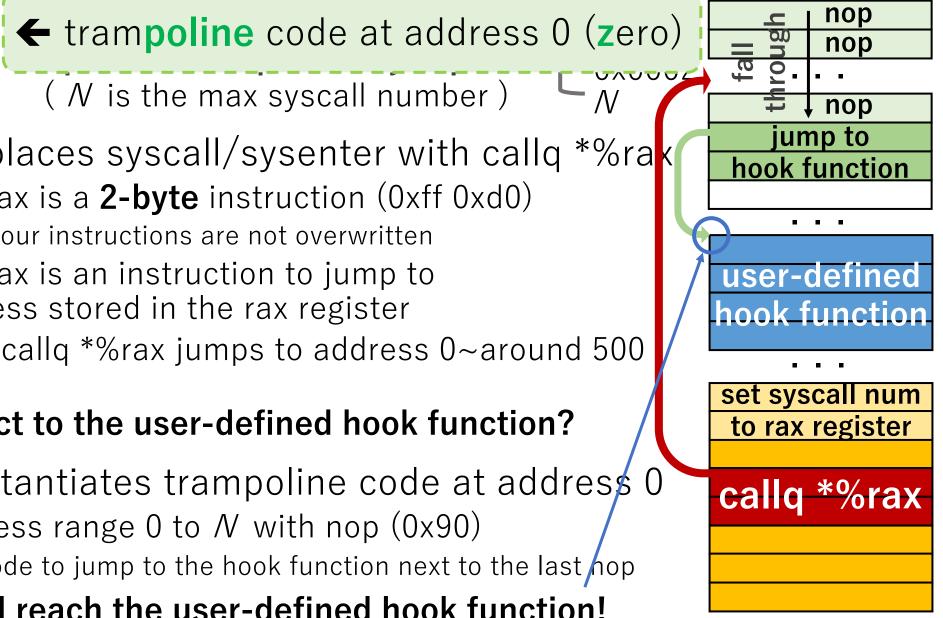
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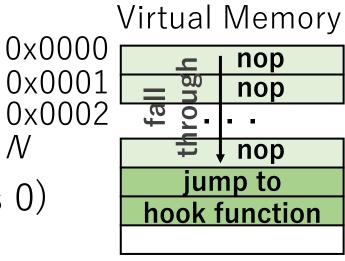
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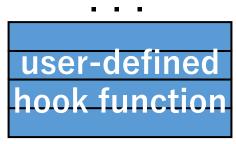
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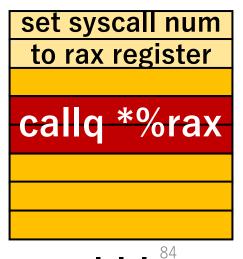


• A buggy program may access NULL (address 0)

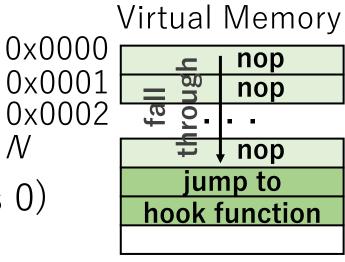


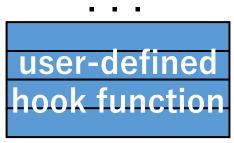


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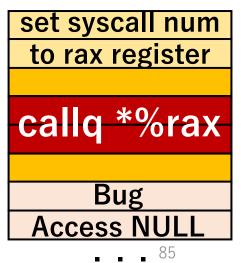


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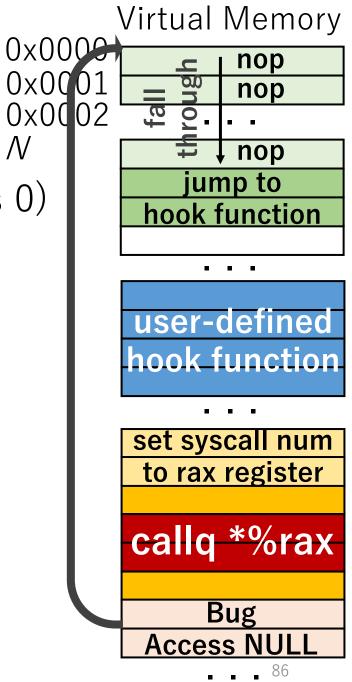




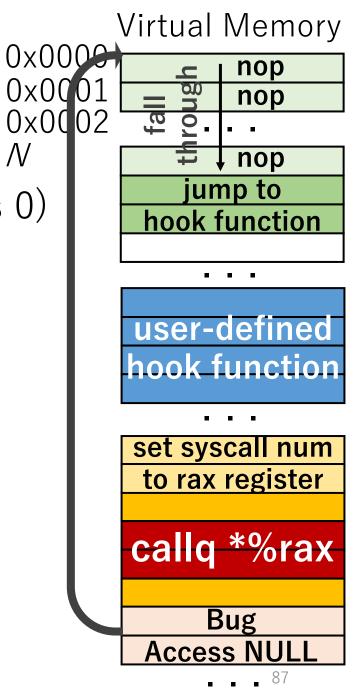
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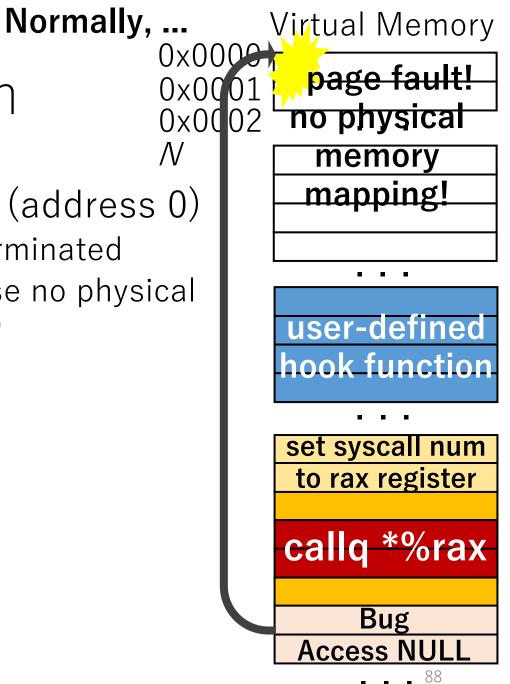
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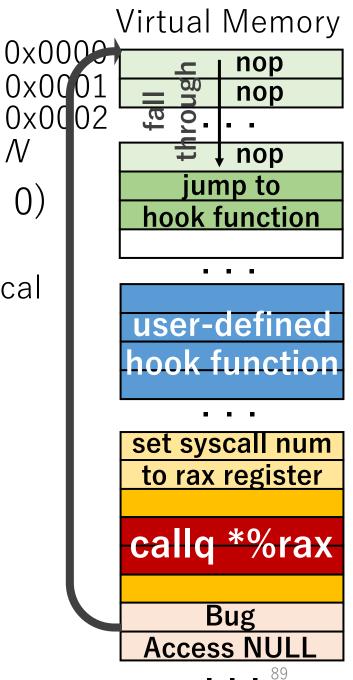
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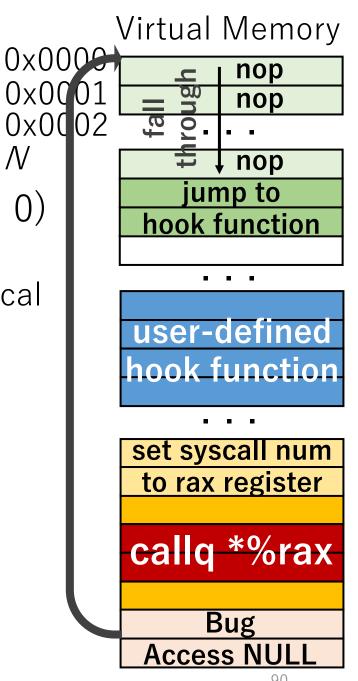
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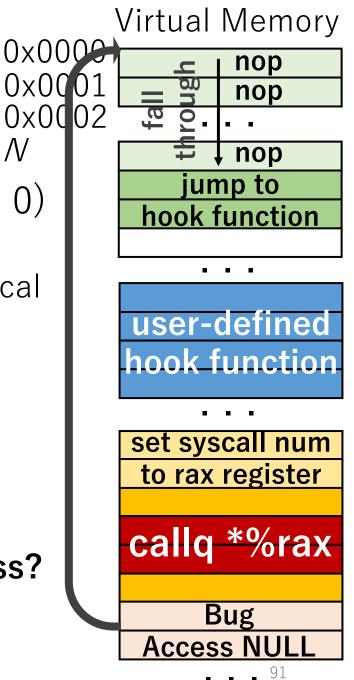


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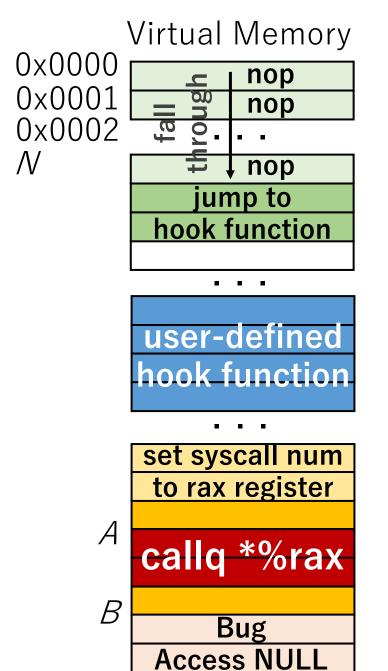


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How can we detect and terminate a buggy NULL access?

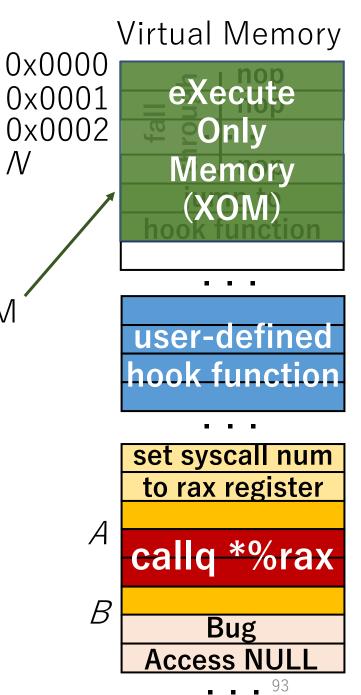


Memory access: read / write / execute

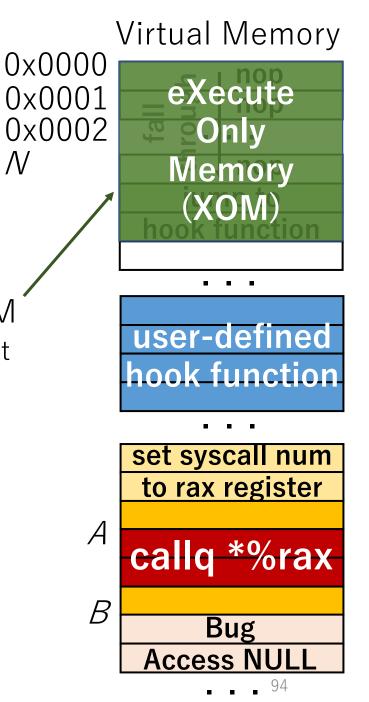


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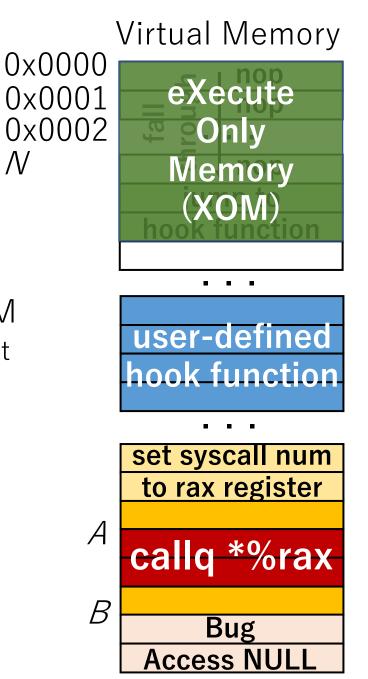
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- Solution
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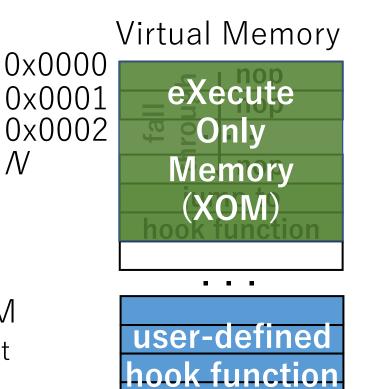


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95

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set syscall num

to rax register

Bug

Access NULL

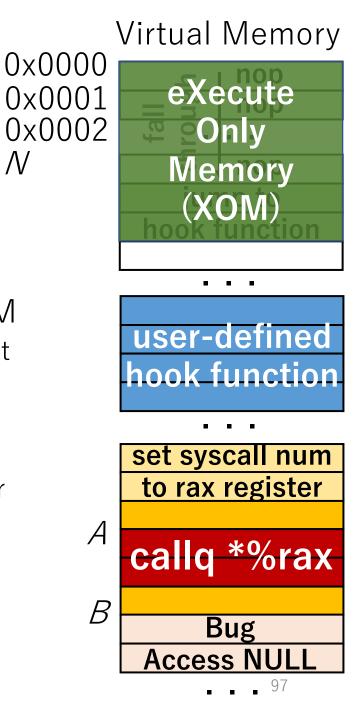
callg *%

A

В

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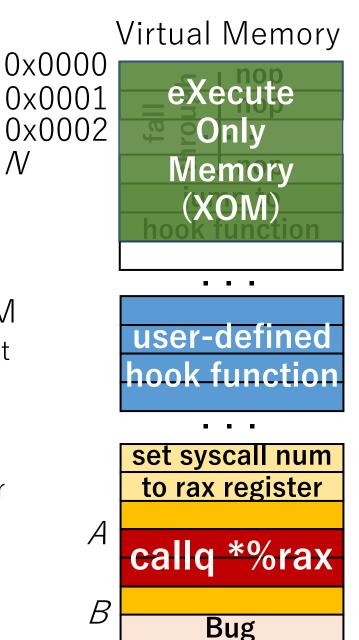
During binary rewriting phase ...



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During binary rewriting phase ...

List of replaced addresses : [...]



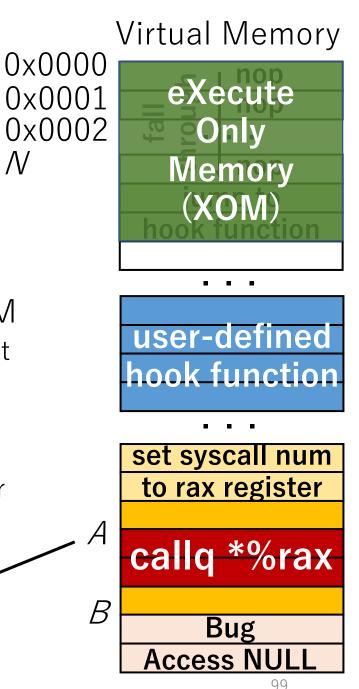
 \mathcal{N}

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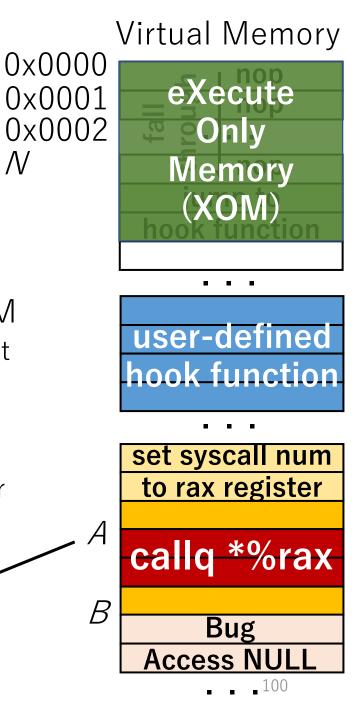
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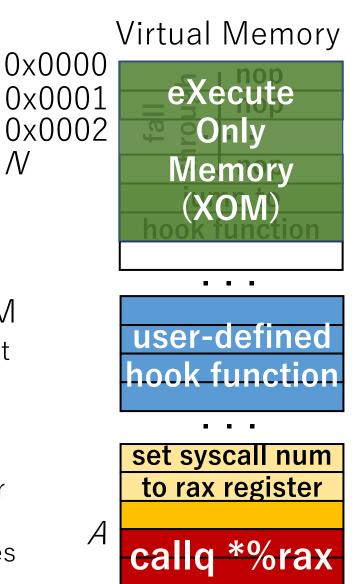
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List of replaced addresses : [A, ...]



Bug

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Access NULL

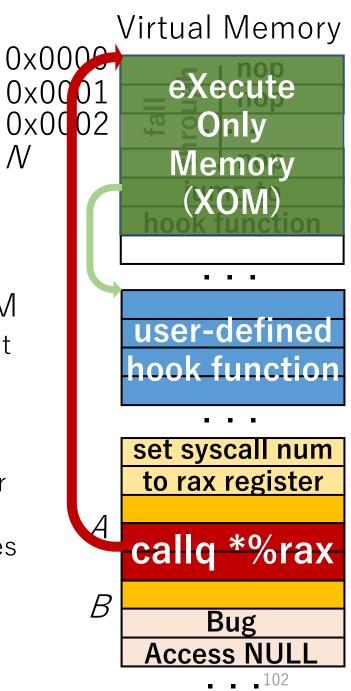
 \mathcal{N}

В

NULL Access Termination **At runtime ...**

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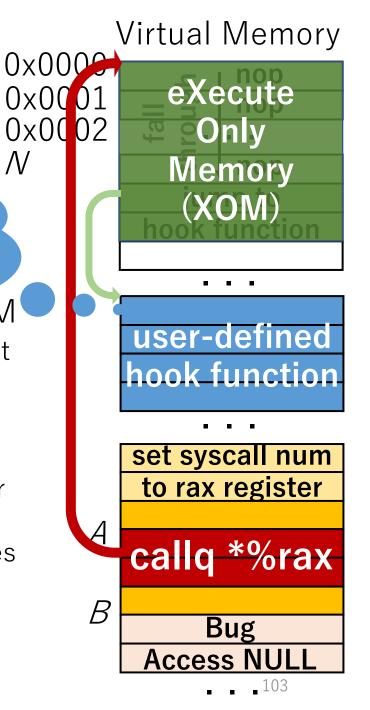
List of replaced addresses : [A, ...]



At runtime ...

- Memory acces
- Solution
 - read/write.
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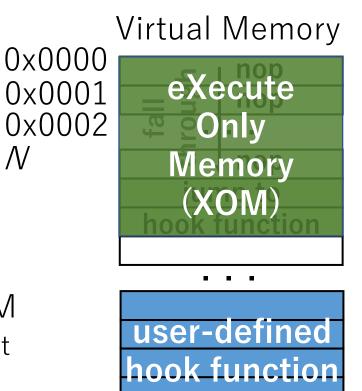
 \mathcal{N}

XOM

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List of replaced addresses : [A, ...]



set syscall num

to rax register

callg *%rax

Bug

Access NULL

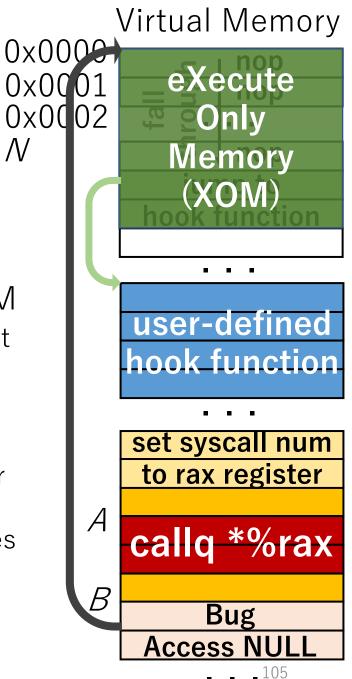
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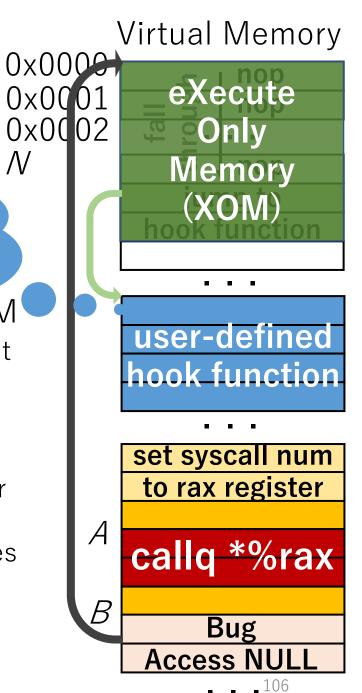
```
List of replaced addresses : [A, ...]
```



At runtime ...

- Memory acces
- Solution
 - read/write.
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 \mathcal{N}

XOM

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Virtual Memory

eXecute

Only

Memory

(XOM)

. . .

set syscall num

to rax register

callg *%rax

Bug

Access NULL

er-defined

0x0000

0x0001

Stop the

XOM

x0002

A

В

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 - Current prototype uses bitmap to implement this check

List of replaced addresses : [A, ...]

Virtual Memory 0x0000 eXecute 0x0001 x0002 Only Stop the Memory (XOM) . . . set syscall num to rax register A callg *%rax

В

XOM

108

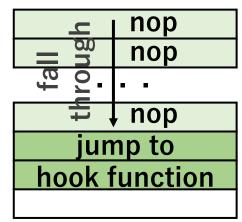
Bug

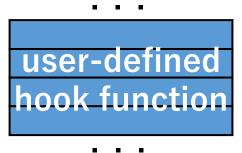
Access NULL

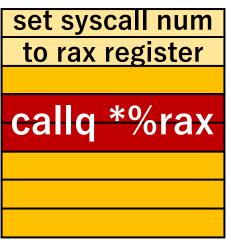
• Time to hook getpid() and return a dummy value

Mechanism	Time [ns]
ptrace	31201
int3 signaling	1342
SUD	1156
zpoline	41
LD_PRELOAD	6

Virtual Memory







• Time to hook getpid() and return a dummy value

Mechanism	Time [ns]	• • •
ptrace	31201 71	6x user-defined
int3 signaling	1342 <mark>3</mark> 2	.7x hook functio
SUD	1156 28	.1x set syscall nun
zpoline	41 improve	ment to rax register
LD_PRELOAD	6	callq *%rax

O

2

fall

nop

nop

nop

_ 110

n

. .

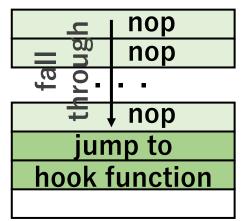
jump to

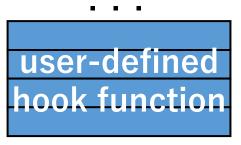
hook function

• Time to hook getpid() and return a dummy value

Mechanism	Time [ns]
ptrace	31201
int3 signaling	1342
SUD	1156
zpoline NULL exec check: 1 ns c	out of 41
LD_PRELOAD	6

Virtual Memory



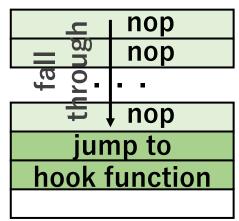


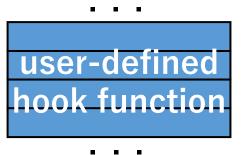


• Time to hook getpid() and return a dummy value

Mechanism	Time [ns]	
ptrace	31201	
int3 signaling	1342	
SUD	1156	
zpoline	41 <u>+35ns</u>	
LD_PRELOAD	6 overhea	d

Virtual Memory







• Time to hook getpid() and return a dummy value

Mechanism	Time [ns]
ptrace	31201
int3 signaling	1342 /
SUD	1156
zpoline	41 <mark>+35ns</mark>
LD_PRELOAD	6 overhead

Virtual Memory nop **D** nop fa 2. nop jump to hook function . . . user-defined hook function . . . set syscall num

to rax register

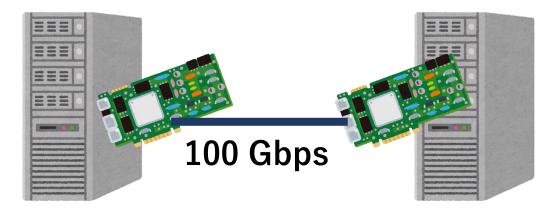
callq *%rax

_ 113

additional

overhead

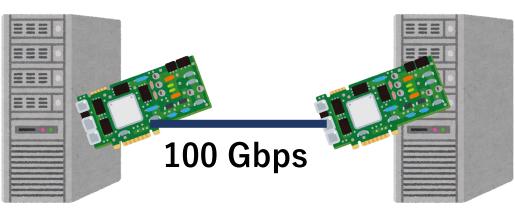
• We **transparently** apply IwIP + DPDK to an application using different system call hook mechanisms



• We **transparently** apply IwIP + DPDK to an application using different system call hook mechanisms

Simple HTTP server

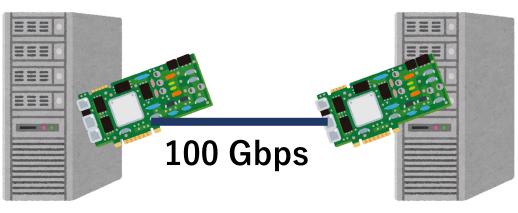
IwIP + DPDK



• We **transparently** apply IwIP + DPDK to an application using different system call hook mechanisms



IwIP + DPDK



• We **transparently** apply IwIP + DPDK to an application using different system call hook mechanisms

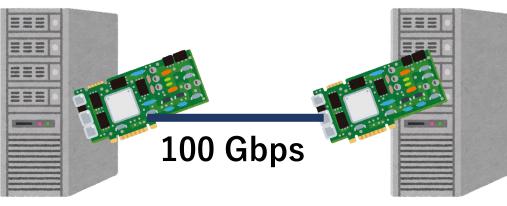
Simple HTTP server

zpoline, LD_PRELOAD wrk: benchmark client

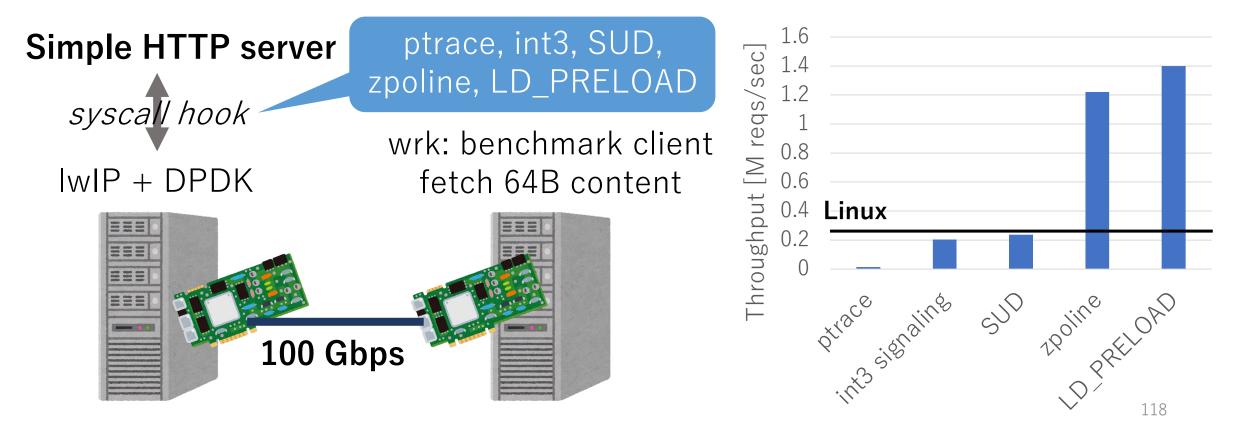
ptrace, int3, SUD,

IwIP + DPDK

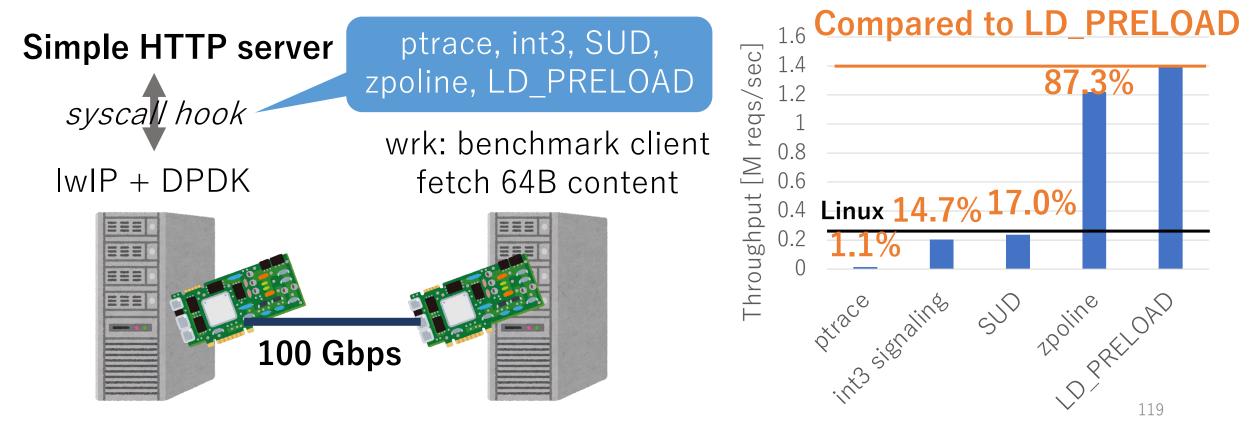
wrk: benchmark clier fetch 64B content



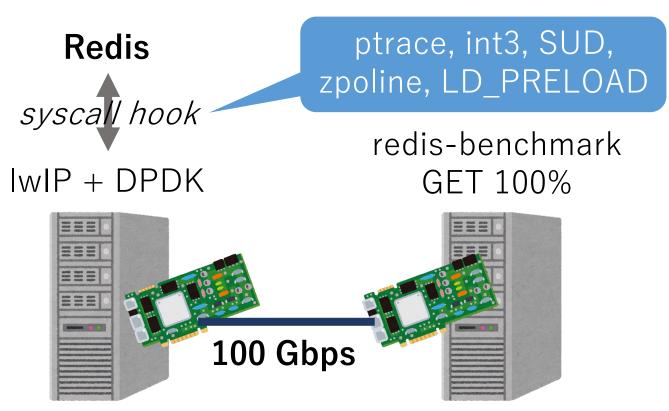
 We <u>transparently</u> apply IwIP + DPDK to an application using different system call hook mechanisms



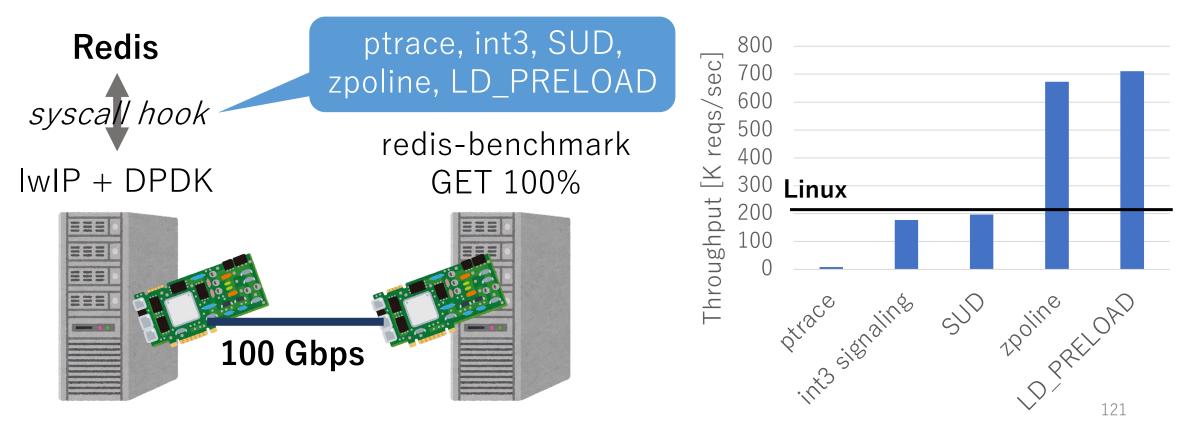
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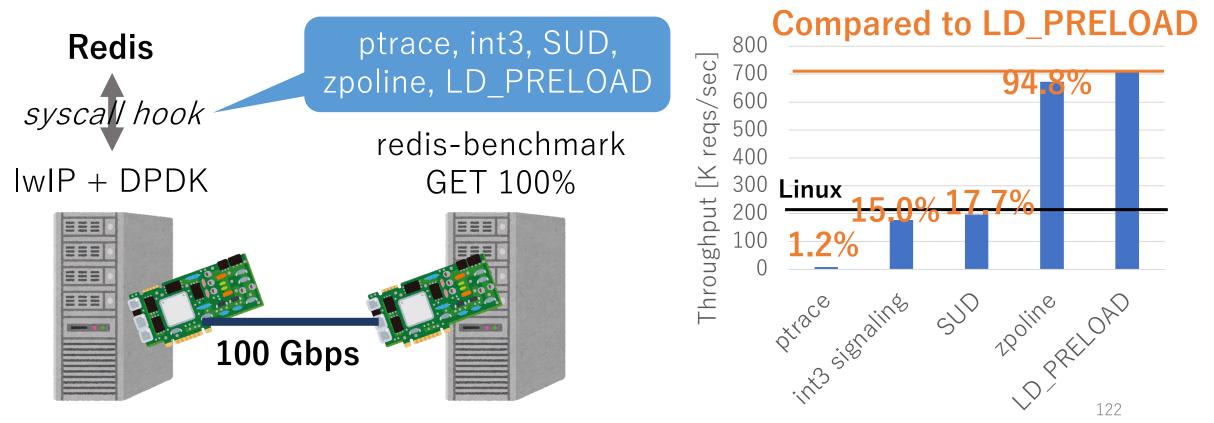
• We **transparently** apply IwIP + DPDK to an application using different system call hook mechanisms



 We <u>transparently</u> apply IwIP + DPDK to an application using different system call hook mechanisms



 We <u>transparently</u> apply IwIP + DPDK to an application using different system call hook mechanisms



Summary

- zpoline: a system call hook mechanism for x86-64 CPUs
 - based on binary rewriting
 - replaces syscall/sysenter with callq *%rax
 - instantiates the tram<u>poline</u> code at virtual address 0 (<u>z</u>ero)
 - free from the drawbacks of the pervious mechanisms
 - keeps the performance benefit of user-space OS subsystems
- Source code: <u>https://github.com/yasukata/zpoline</u>
 - since October 2021



Speeding up the Trampoline Code

- Inspired from USENIX ATC'23 reviewers who suggested to employ a one-byte short jump instruction for speeding up
 - Put it on the addresses corresponding to obsolete system calls
- Optimization: repeat **0xeb 0x6a 0x90** instead of nops
 - Hook overhead reduction from 41 ns to 10 ns

