



MGG: Accelerating Graph Neural Networks with Fine-grained Intra-kernel Communication-Computation Pipelining on Multi-GPU Platforms

Yuke Wang, Boyuan Feng, Zheng Wang, *Tong Geng, ^Kevin Barker, ^Ang Li, Yufei Ding.

*: University of Rochester, ^:Pacific Northwest National Laboratory University of California, Santa Barbara



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Graphs are everywhere, and GNN is the key!





Social Networks

Power Grid

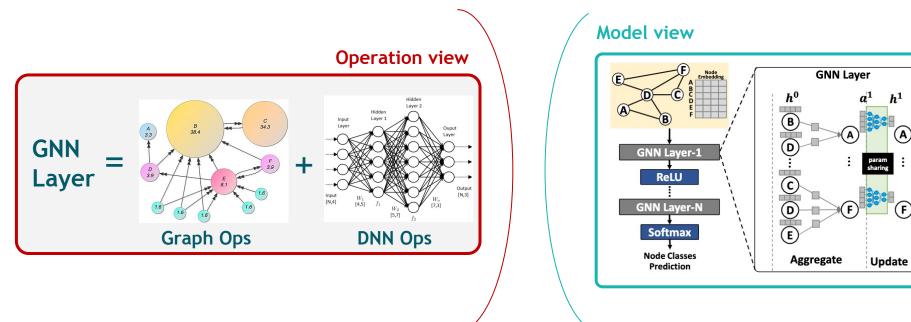
Financial Services

Molecular Biology





GNN: Graph Neural Networks



 h^1

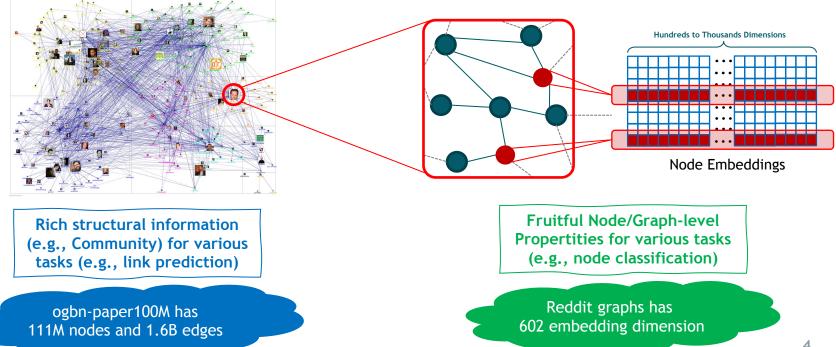
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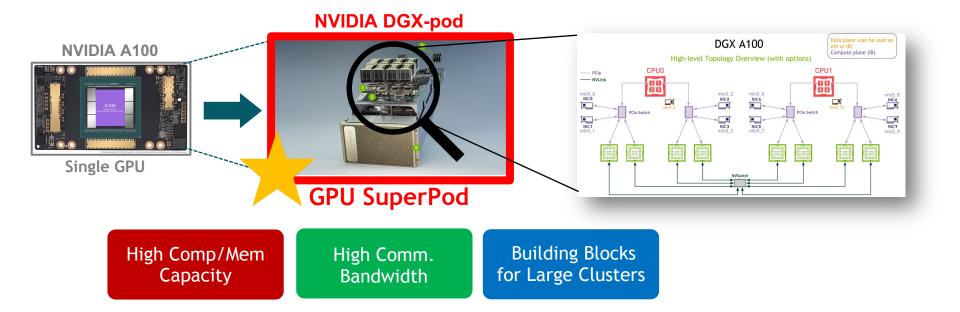
GNNs are Scaling Up!

1. Graph Structural Scaling. \geq

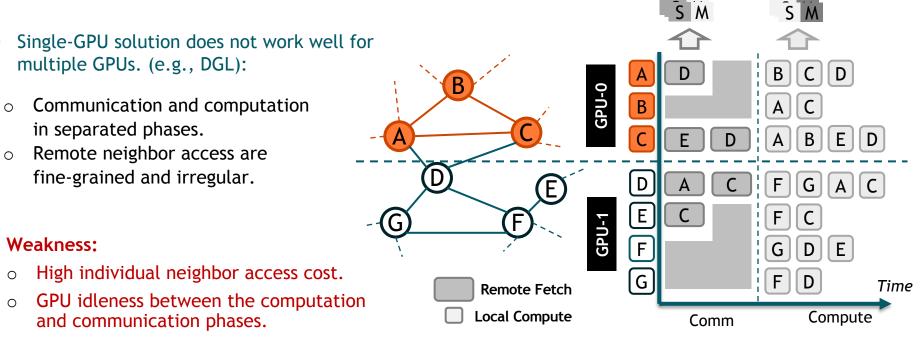
2. Graph/Node Embedding Scaling. \geq



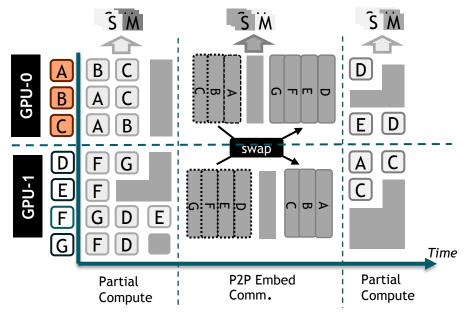
DL Infrastructures are Scaling to Catch Up!



Powerful Multi-GPU Platforms Cannot Solve Everything!



Traditional Distributed Graph Solutions Do Not Work Well!

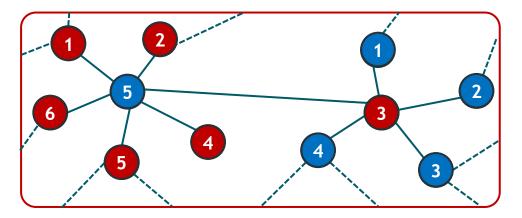


- Schedule Transformation for Dense Communication (e.g., NeuGraph, P3, ROC):
 - Neighbor aggregation is divided into multiple rounds.
 - Neighbor movement are dense, regular and coarse-grained.
 - Neighbor access in each round of aggregation is all local.

Weakness:

- Additional algorithmic modification.
- o Redundant data movements.
- Decreased computation efficiency,
- Ma, Lingxiao, et al. "NeuGraph: Parallel Deep Neural Network Computation on Large Graphs." USENIX Annual Technical Conference. 2019.
- Gandhi, Swapnil, and Anand Padmanabha lyer. "P3: Distributed Deep Graph Learning at Scale." OSDI. 2021.
- Jia, Zhihao, et al. "Improving the accuracy, scalability, and performance of graph neural networks with roc." Proceedings of Machine Learning and Systems 2 (2020).

Algorithm-Observation: Opportunity for Fine-grained Pipelining



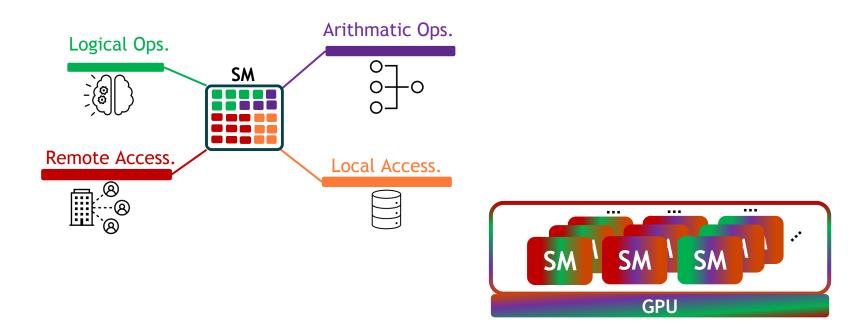
Fine-grained neighbor aggregation dependency.

AC: Aggregate Computation

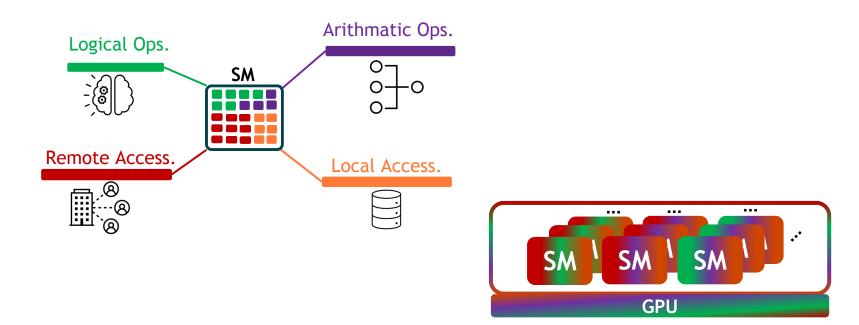
LL: Load Local Neighbor

New opportunities: we can amortize communication costs by fine-grained overlapping neighbor aggregation from different nodes.

Hardware-Observation: SM Multiplexing for Operation Overlapping



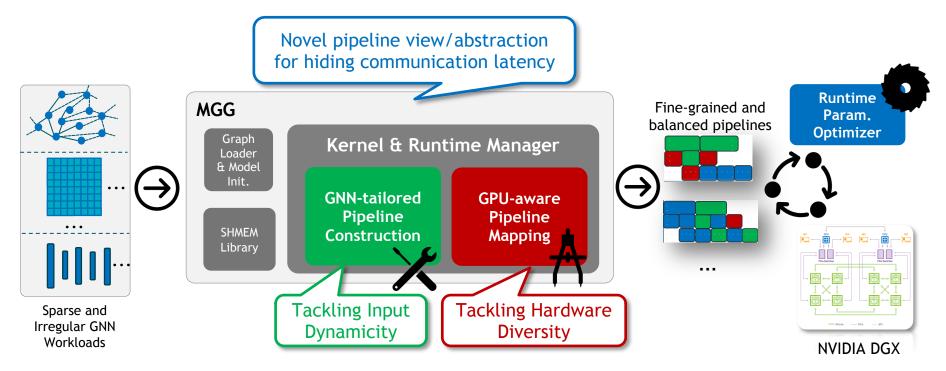
Hardware-Observation: SM Multiplexing for Operation Overlapping



Hardware-Observation: SM Multiplexing for Operation Overlapping



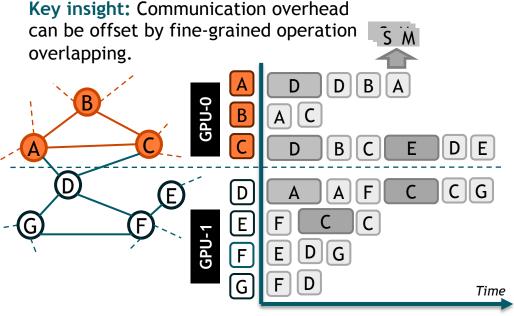
MGG Overview



Contribution-1: Pipeline View and Abstraction.

Neighbor Access Latency



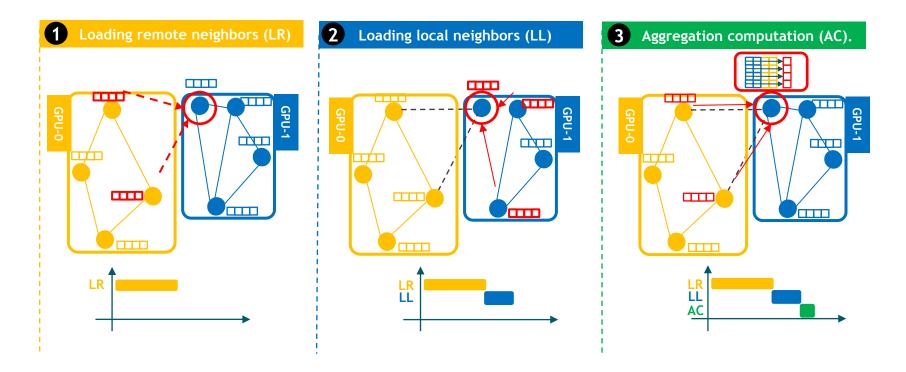


Challenges:

- Communication overweight the local computations/access and dominate the execution.
- Communication exacerbate the workload imbalance.

- The idle cycles of GPUs communication can be fulfilled by other local computing.
- Multi-GPU GNN workload can be abstracted as a fine-grained dynamic software pipeline.

A three-stage dynamic software pipeline.

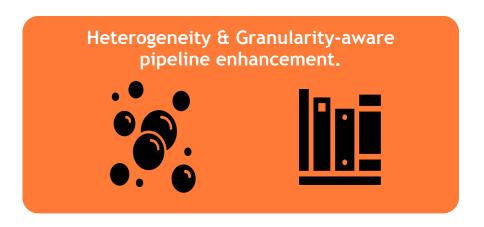


Contribution-2: Pipeline-aware Workload Management.



Challenge: Input diversity (e.g., graph size/sparsity) would affect the pipeline efficiency (e.g., bubble ratio).

Key Insight: Pipeline can be tailored for maximizing efficiency based on diverse GNN inputs properties (e.g., #nodes, #edges and node degree).

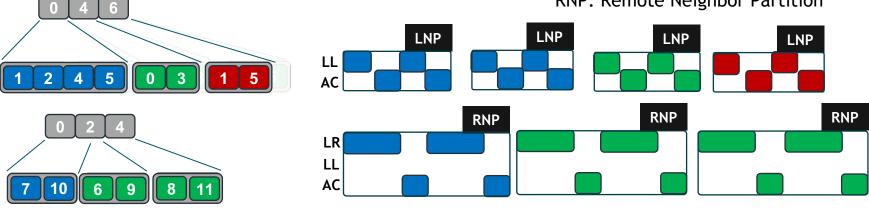


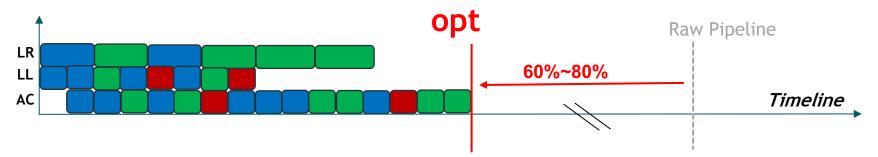
Heterogeneity & Granularity-aware Pipeline Enhancement.



Facilitate a more balanced workload distribution among pipeline stages

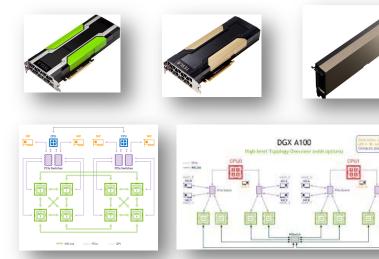
LNP: Local Neighbor Partition RNP: Remote Neighbor Partition





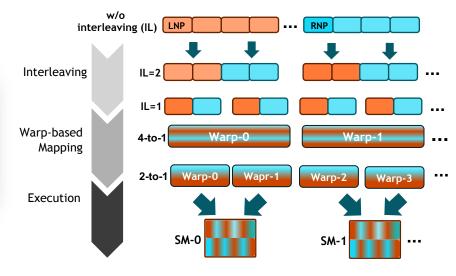
Contribution-3: GPU-Aware Pipeline Mapping.

Hardware Diversity



Challenges: Hardware diversity (e.g., Comp/Comm Speed) would affect the pipeline execution performance (e.g., SM utilization and occupancy).

Key insight: Dynamically configurable pipeline-workload-to-SM mapping can maximize pipeline execution performance.



Intelligent Runtime Design.

Specialized Memory Design & Optimization.

Evaluation

Dataset	#Vertex	#Edge	#Dim	#Class
reddit(RDD) [45]	232,965	114,615,892	602	41
enwiki-2013(ENWIKI) [23]	4,203,323	202,623,226	300	12
it-2004 (IT04) [10]	41,291,594	1,150,725,437	256	64
ogbn-paper100M(PAPER) [12]	111,059,956	1,615,685,872	128	64
ogbn-products(PROD) [17]	2,449,029	61,859,140	100	47
ogbn-proteins(PROT) [17]	132,534	39,561,252	8	112
com-orkut(ORKT) [23]	3,072,441	117,185,083	128	32

> GNN Models.

- Graph Convolutional Network (GCN):
 2 layers with 16 hidden dimensions.
- Graph Isomorphism Network (GIN):
 5 layers with 64 hidden dimensions.

> Evaluation Platform.

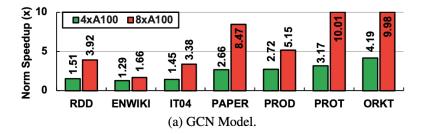
 NVIDIA DGX-A100 with dual AMD Rome 7742 processors (each with 64 cores, 2.25 GHz), 1TB host memory, and 8x(A100-40GB) connected via NVSwitch.

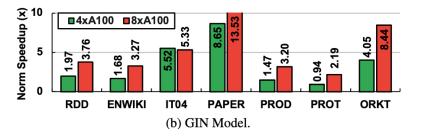
Baseline.

- Deep Graph Library [ICLR'19].
- Unified Memory [NVIDIA].
- ✤ ROC [MLSys'20].

Evaluation: Overall Performance

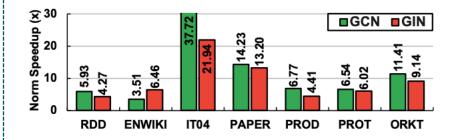
 Compare with DGL. (Seperated Communication and Computation)





Averaged 4.41x speedup in comparison with DGL on GCN and GIN.

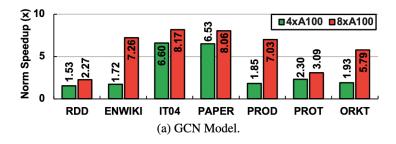
 Compare with ROC. (Schedule Transformation for Dense Communication)

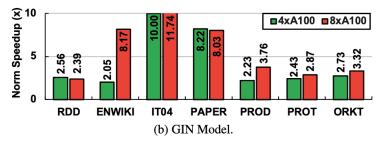


Averaged 10.83x speedup in comparison with ROC (8xA100) on GCN and GIN.

Evaluation: Additional Comparisons

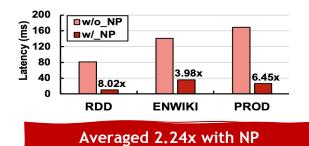
Compare with MGG-UVM.
 (Pipeline with Coarse-grained Communication)



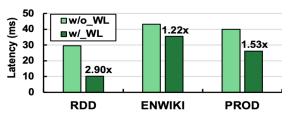


Averaged 4.81x speedup in comparison with MGG-UVM on GCN and GIN.

Neighbor Partitioning (input Dynamicity)



Workload Interleaving (Hardware Diversity)



Averaged 1.88x with WL

Contribution Summary

Exploting the joint optimization of the communication and computation.

A novel and unique multi-stage pipeline view/abstraction

Capatializing pipelining benefits for input dynamicity.



GNN-tailored pipeline construction

Enhance pipeline efficiency for diverse hardware.



GPU-aware pipeline mapping



Thank You

Q&A

Contact: yuke_wang@cs.ucsb.edu Code: https://github.com/YukeWang96/MGG-OSDI23-AE.git

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